

Face-to-Face Meeting

Cortus

Le Génésis,

97 rue de Freyr

34000 Montpellier

September 19-20, 2016

CONTINUUM
A FRENCH ANR PROJECT

Design Continuum for Next Generation Energy-Efficient Compute Nodes

Monday, September 19th	
09:30	Introduction – Coordinator <ul style="list-style-type: none">▪ Agenda and objectives of the meeting▪ Project progress by WP, review of deliverables, milestones
10:00	Compilation techniques for exploiting NVMs (Inria Rennes) <ul style="list-style-type: none">▪ Implementation of silent stores in LLVM: principles and implementation
11:00	Coffee Break
11:15	Emerging memory technologies (LIRMM) <ul style="list-style-type: none">▪ Loop optimization in presence of SST-MRAM caches: performance and energy tradeoffs▪ On scratchpad memory in multicore systems (brief state-of-the-art)
12:30	Lunch
14:00	Implementation of heterogeneous multicore systems with Cortus technology (Cortus) <ul style="list-style-type: none">▪ Towards a heterogeneous architecture based on Cortus cores▪ Simple demo
15:00	<ul style="list-style-type: none">▪ Discussion on improvements for exploiting proposed architecture model (All)<ul style="list-style-type: none">○ Desirable architecture features○ How to apply considered compilation techniques
16:00	Coffee Break
16:15	<ul style="list-style-type: none">▪ Integration of Cortus technology in gem5 (Cortus)
17:15	Ajourn
Dinner (to be announced)	
Tuesday, September 20 th	
09:30	Technical discussion (All) <ul style="list-style-type: none">▪ Dynamic compilation, temporary non volatility, integration with Cortus technology▪ Evaluation of preliminary project results: current obstacles and actions to be taken
11:00	Coffee Break
11:15	Further discussion (All) <ul style="list-style-type: none">▪ Management: annual report, review preparation, etc.▪ Dissemination plan: announcements, papers, etc.▪ Next meeting
12:00	Lunch & closing