



Call for Special Sessions

15th IEEE International Conference on Design & Technology of Integrated Systems in Nanoscale Era



General Chair:

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Key Dates:

Special Session

proposal submission:

~~January 23, 2020~~

January 31, 2020

Notification:

February 22, 2019

Camera Ready:

March 15, 2019

The aim of the conference is to cope with technology scaling limits and the emergence of novel paradigms. The areas of interest include design, technology, test and dependability of electronic products. The proposed work must introduce new concepts concerning methodologies, tools and theoretical approaches for design, verification and validation.

Special Session proposal: DTIS 2020 seeks proposals for Special Sessions. The special sessions should aim at providing a complementary experience with respect to the regular sessions by focusing on hot and emerging topics of interest to the Nanotechnology Community (emerging technologies, approximate computing, beyond Von Neumann architectures such as neuromorphic and in-memory computing), as well as on multidisciplinary topics, that are expected to have a significant impact on the future technologies. A special session could consist of a set of individual presentations or a panel, possibly including experts from the industry.

Submission Guidelines: Proposals for original, high-quality Special Sessions addressing innovative research, highlighting significant topics and emerging issues, related to the conference theme, should not exceed two pages, should describe the topic and intended audience, and include a short bio of organizer(s). All special session proposals should be sent to the program chair within the standard paper submission deadline (January 23, 2020).

Publication: Upon acceptance, special session presenters can prepare either a single paper for the entire session or one paper per presenter to be included in the formal proceedings. For this reason, papers (presenting original and unpublished contributions and that may be 4 pages or 6 pages long) for special session will go through review process. Accepted papers will appear in the formal proceedings of DTIS 2020 symposium. Proceedings will be published by the IEEE and appear in the IEEEExplore Digital Library.

Journal Special Issue: An extended version or developments of the accepted papers appearing in the conference proceedings are invited to participate to a special issue of the Microelectronics Reliability Journal by Elsevier editor.

www.lirmm.fr/dtis2020/

Questions about the conference can be addressed to:

General Chair

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