



12th International Conference on Field Programmable Logic and Applications

September 2-4, 2002
Montpellier (La Grande Motte), France

CALL FOR PAPERS & TUTORIALS

The major International Conference on Field Programmable Logic and Applications will be held at the conference center of La Grande Motte (Montpellier), France in September 2002. This annual event provides a forum for presenting and discussing trends, emerging results and hot topics on all aspects of FPL including applications, technology and tools. The twelfth edition is organized by the Microelectronics department of the Laboratory of Informatics, Robotics and Microelectronics of Montpellier (LIRMM) and sponsored by the University of Montpellier, electronic industries and local authorities.

Conference Theme:
Heading toward the 50 million gate FPGA
New Applications, New EDA Achievements, Test and Verification Issues

We cordially invite you to participate and submit your contribution to FPL'02, which includes (but is not limited to) the following topics:

A) Reconfigurable Hardware and Systems:

- Run-time reconfigurable architectures
- Reconfiguration techniques
- Dynamically reconfigurable architectures
- Low power reconfigurable circuits and systems
- Customizable computing engines
- Embedded reconfigurable systems
- Fault tolerant designs
- Field-programmable analog arrays

B) Applications:

- Applications in communication/networking
- Real-world applications of evolvable hardware
- Rapid prototyping
- Mobile applications
- Core-based architectures on FPGAs, SOPCs
- Algorithms implemented on FPGAs

C) CAD Compilation, Testing and Verification:

- CAD for FPGA-based application development
- Design flow
- Optimization, technology mapping, system-level partitioning
- Testing, verification and benchmarking of reconfigurable circuits and systems
- Hardware/software co-evolution, genetic algorithms/programming

D) Surveys, Trends and Education:

- Roadmaps to technology, applications and design
- Teaching reconfigurable systems and evolutionary computing
- History of reconfigurable logic and related subjects
- Biologically inspired approaches to FPL
- Tutorials

Submissions:

The Program Committee solicits submissions describing novel research and development as well as applications and educational efforts.

- For paper submission, authors are invited to submit a PDF file (10 pages max) via E-mail to: fplsubmit@mes.tu-darmstadt.de (guidelines at <http://www.springer.de/comp/lncs/authors.html>)
- For tutorial proposals, authors are invited to submit a short description (2 pages) via E-mail to: glesner@mes.tu-darmstadt.de

Proceedings:

The proceedings will be published by Springer Verlag within the LNCS series (Lecture Notes on Computer Science).

See: <http://link.springer.de/series/lncs/>

Deadlines and Key Dates:

- Submission of tutorials: **February 15, 2002**
- Submission of papers: **March 28, 2002**
- Notification of acceptance: **May 20, 2002**

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Please visit the FPL 2002 web page at: <http://www.lirmm.fr/fpl2002>

