Laboratoire d'Informatique de Robotique et de Microélectronique de Montpellier
LIRMM

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UNIVERSITÉ MONTPELLIER II
SCIENCES ET TECHNIQUES DU LANGUEDOC

TECHNISCHE UNIVERSITÄT DARMSTADT

MES — Institute of Microelectronic Systems

Call for Papers

2nd International Workshop on Reconfigurable Communication Centric System-on-Chips

http://www.lirmm.fr/RECOSOC06

Submission deadline: May, 12   Notification of acceptance: June, 12

Goals:

ReCoSoC aims to provide a prospective view of tomorrow's challenges in the multi-billion transistor era, taking into account the emerging techniques and architectures exploring the synergy between flexible on-chip communication and system reconfigurability.

Topics of interest include:

- Embedded Reconfigurability in all its forms
- On-chip communication architectures
- System & SoC design methods
- Asynchronous design techniques
- Low-power design methods
- Middleware and OS support for reconfiguration and communication
- New paradigms of computation including bio-inspired approaches

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ReCoSoC renew its first edition principle: thanks to the financial support of our sponsors, registration fees will remain very low and the first author of the ten best papers will have registration fee completely waived.

Deutsche Forschungsgemeinschaft DFG

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