Perfect Hashing for Method Dispatch with Dynamic Typing and Dynamic Compilation

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Abstract

In static typing, the receiver’s static type is the key to efficient implementation of method invocation, and a recently proposed technique, based on perfect hashing of classes, cannot apply to dynamic typing because of the lack of static types. In this article, we propose a new application of perfect hashing to method dispatch in a dynamic typing, dynamic loading and single inheritance setting. The approach involves hashing method selectors instead of classes. However, as hashing all methods revealed itself to be space-inefficient, only overloaded methods, ie methods introduced by several classes, are hashed. The dispatch of non-overloaded methods is done as in single-subtyping, ie static typing and single inheritance.

An adaptive-compilation protocol and an algorithm for hashing overloaded methods are proposed, and the approach is tested on SMALLTALK benchmarks by simulating class loading at random.

Keywords: method dispatch, single inheritance, dynamic typing, object representation, compilation protocol, perfect hashing, perfect numbering, adaptive compiler.

1 Introduction

In spite of its 30-year maturity, object-oriented programming still has a substantial efficiency drawback in the dynamic loading context and it is worsened by either multiple inheritance, or dynamic typing. In the dynamic loading context, compilation must be separate (as opposed to global), ie code units are compiled separately from each other. Compilation can also be static or dynamic. With static compilation, each code unit is compiled once for all, and this implies the open-world assumption (OWA) which makes the generated code rather inefficient. In contrast, with dynamic compilation, a code unit can be further recompiled; it thus allows the compiler to perform aggressive optimizations, based on provisory closed-world assumptions (CWA), which makes the code more efficient, at the price of extra recompilations. The overall efficiency is thus a tradeoff between the runtime efficiency of the generated code and the recompilation cost. This article focuses on both dynamic compilation and dynamic typing.
In recent articles, we proposed a new implementation approach, called *perfect class hashing* [1] and based on *perfect hashing*, i.e. truly constant-time, collision-free hashing [2, 3]. From an algorithmic point of view, a variant called *perfect numbering* involves optimizing the class IDs in order to minimize the hashtable sizes [4]. Real-size experiments in the PrIM compiler [5] showed that perfect hashing would be quite efficient for implementing *multiple subtyping*, i.e. Java interfaces, in a static, separate compilation setting. However, it would be rather inefficient, i.e. as inefficient as C++-like subobjects, when used in a full multiple-inheritance context [6]. Therefore, we also proposed a compilation/recompilation protocol that would allow for an efficient implementation in a just-in-time, dynamic compiler [7]. Indeed, in this dynamic setting, most invocation sites can use shortcuts that are more efficient than perfect hashing.

However, perfect class hashing can be used only in a static typing setting, because it involves hashing classes, and grouping methods by introduction class. On a given invocation site, the introduction class is thus deduced from the receiver's static type. In contrast, even with single inheritance, in a *dynamically typed* language like Smalltalk [8], a method (i.e. a method selector in Smalltalk jargon) may be introduced by several classes. Hereafter, we will say that such methods are *overloaded*. Therefore, the perfect hashing approach requires, in this new context, an efficient way of hashing methods instead of classes, and first experiments showed that perfect method hashing was not that space-efficient [1].

In this paper we propose an object representation and a recompilation protocol which provide an efficient use of perfect method hashing for implementing Smalltalk-like languages. The main idea is that perfect hashing is used only for overloaded methods, which are presumed to be few enough for keeping the overhead low. However, as the overload feature depends on the classes that are actually loaded, these hashables must be recomputable, and an extra indirection is required. In contrast, methods that are introduced by a single class are invoked in the same way as in single-subtyping (SST), by deducing the single introduction class from the method selector instead of the receiver's static type. Besides, common optimizations like monomorphic invocations can be considered.

**Plan.** The rest of the article is structured as follows. The next section presents the object-representation issue, and states our proposal of applying perfect hashing (more precisely, a variant called *perfect numbering*) to overloaded methods. Section 3 presents the compilation/recompilation protocol the technique requires in an adaptive compiler. Section 4 recalls the perfect hashing problem, formalizes the new algorithmic problem and proposes a heuristic which is, in some sense, optimal, though non-optimal on many aspects. Section 5 presents our experiments, based on a simulation of large benchmarks with random class-loading. These tests show that overloaded methods are few enough, thus making the overhead of perfect hashing very low. Furthermore the algorithm appears to

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1 This use of the overload term must not be confused with *static overloading*, which represents, in languages like C++, C# or Java, the fact that a method name can be used in the same context with different parameter types or numbers. It must not either be confused with *overloaded functions* which represent methods dispatched on all their parameters [9]. Finally, it must not be confused with *overriding* which represents the fact that a method defined in a class can be redefined in a subclass.
be space-efficient enough to be further considered. Conclusions and prospects
end the article.

2 Object and value representation

2.1 Object representation

**Single subtyping.** Figure 1 describes the implementation used in single-
subtyping (SST), i.e. in static typing, when all types are classes and have a single
direct supertype. This implementation is simple and efficient, as it verifies the
*position invariant*: the considered method or attribute is always implemented
at the same position, whatever the receiver’s dynamic type. In this implementa-
tion, methods and attributes are grouped by introduction class (type) and the
group of a class is appended to the structure of its direct superclass. Subtype
testing is implemented with the technique known as *Cohen’s display* [10], with
the class ID in each method group. However, the SST approach cannot be used
either with multiple inheritance or dynamic typing.

**Perfect class hashing in static typing.** Figure 2 describes the implementa-
tion of method invocation and subtype testing with perfect class hashing, in
a static typing setting. It can be used directly for multiple subtyping (i.e. JAVA
interfaces). In the method tables, the positive offsets contain the SST imple-
mentation which is used for class-typed invocations. It can also be extended to
attribute access for full multiple inheritance, but this extension (called accessor
simulation) is not that efficient. In [7], we proposed to use this implementa-
tion in a dynamic loading setting, in conjunction with a recompilation proto-
col which allows the compiler to shortcut most of the polymorphic PH invocations
with SST invocations. This optimization is possible when the position invariant
holds, and it is made efficient by the fact that it holds most of the time. Of
course, all monomorphic method calls\(^2\) are shortcut with static calls. It would
thus avoid most of the uses of PH in actual invocations. Anyway, this approach
cannot work with dynamic typing.

**All-method perfect hashing in dynamic typing.** Figure 3 describes the
implementation of method invocation and subtype testing in dynamic typing,
with perfect method hashing. In single inheritance, it would be more efficient
to remove class IDs from the hashtable and put them as in SST but in negative
offsets. In multiple inheritance, the hashtable must be extended for attribute
access, with accessor simulation, but it works only with SMALLTALK-like encaps-
ulation. This new approach would work with dynamic typing but it would not
be that efficient. Indeed, with this implementation, there is no way to shortcut
polymorphic PH invocations with a more direct invocation sequence, because
the position invariant does not hold.

\(^2\)An expression is said to be monomorphic when its value at runtime will always be of the
same dynamic type. When the receiver is monomorphic, a virtual call always invoke the same
method. Here, we use monomorphic in a wider meaning, that is when a virtual call always
invoke the same method (even when the receiver is not monomorphic).
// attribute access
load [object + #attOffset], attVal

// method invocation
load [object + #tableOffset], table
load [table + #methOffset], methAddr
call methAddr

// subtype test
load [object + #tableOffset], table
load [table + #classOffset], id
cmp id, #targetId
bne #fail

// succeed
Code sequences for the 3 basic mechanisms and the corresponding diagram of object layout and method table. The pseudo-code is borrowed from [11]. Pointers and pointed values are in Roman type with solid lines, and offsets are italicized with dotted lines.

Figure 1: Single-subtyping implementation

// preamble for both mechanisms
load [object + #tableOffset], table
load [table + #hashingOffset], h
and #interfaceId, h, hv
sub table, hv, htable

// subtyping test
load [htable + #htOffset-fieldLen], id
cmp #interfaceId, id
bne #fail

// method invocation
load [htable + #htOffset], itable
load [itable + #methOffset], methAddr
call methAddr

The method table is bidirectional. Positive offsets contain method addresses and class IDs, as in SST, and negative offsets consist of the hashtable, with a twofold entry for each implemented interface. The grey rectangle denotes the group of methods introduced by the considered interface. fieldLen represents the entry size, e.g., 8 if 32-bit integers are used. In practice, all numbers (i.e., H and class ID’s) must be multiplied by fieldLen (of course, it works only if it is a power of 2).

Figure 2: PH for JAVA interfaces

// method invocation
load [object + #tableOffset], table
load [table + #hashingOffset], h
move #methId, methId
and methId, h, hv
add table, hv, htable
load [htable + #htOffset], methAddr
call methAddr

The method table consists of the hashtable, which contains a single entry per method or class. With dynamic typing, the method identifier (methID) must be checked in the method prologue, unless the method has been introduced by the hierarchy root.

Figure 3: PH of all methods for dynamic typing and multiple inheritance
// method invocation for overloaded methods
load [object + #tableOffset], table
load [table + #hashingOffset], h
load [table +#htOffset], htable
move #methId, methId
and methId, h, hv
add htable, hv, htable
load [htable], methAddr
call methAddr

// other invocations are like
// with SST

The method table is the same as with SST, apart from two extra fields for referencing the hashtable (#htOffset) and the hashing parameter (#hashingOffset), which can be loaded in parallel on processors that provide instruction-level parallelism. The hashtable contains a single entry per method, only for methods that have been introduced by more than one class.

Figure 4: PH of overloaded methods for dynamic typing and single inheritance

PH restricted to overloaded methods. Finally, Figure 4 presents the implementation we propose in a Smalltalk-like setting. Like the multiple-subtyping implementation (Figure 2), it combines the SST implementation with a hashtable. However, instead of being inlined in the negative offset of the method table, the hashtable is now a separate table, and is thus accessed via an indirection. This is of course less efficient, mostly because of ensuing cache-misses, but the hashtable is now restricted to methods that are introduced by several classes, and it should be used only marginally.

2.2 Special cases

Besides this general object representation, dynamic typing yields a few specific cases which complicate the generated code and add overhead. These points are irrelevant for attributes when they are encapsulated, ie accessible only on self, as in Smalltalk.

2.2.1 Unknown-method exception

In dynamic typing, the compiler cannot ensure that the receiver knows the invoked method. A dynamic check is thus necessary, in most cases. When the method address is gotten from the hashtable, it may result from an empty entry, and instead of testing that this entry is empty, it is better to fill reachable, empty entries with the address of a function which signals the unknown method exception\(^3\). When an actual method address is gotten from the hashtable or the SST method table, it may correspond to another method selector, instead of the invoked one. Therefore, a test must be made at the method entrypoint,

\(^3\)In Smalltalk, a doesNotUnderstand message is sent to the receiver.
and the best way to do it is to pass the method selector in a register and test it against the expected selector as the first instruction of the method body\(^4\).

### 2.2.2 Primitive values

In dynamic typing, the type of values must be encoded in the value itself, and it would be quite inefficient to use, for integers or characters, the same representation as for true objects. Therefore the type is encoded in the bit-representation of the value, instead of being encoded in the method table. A practical solution, for instance, involves reserving the leftmost byte as a type-tag, with a leftmost bit at 1. Hence, a primitive value is a negative number, whereas a reference is positive.

Then a method invocation sequence begins with testing whether the receiver is tagged, i.e., a negative number, then extracts the tag with a bit-wise mask and a shift. Several alternative techniques can then be used, and they can be mixed. A method table can be associated with each primitive type, and accessed via a table of tables, with the tag as an offset. Conversely, a table can be used for each method, with an entry for each primitive type (Figure 5, left). When the number of expected tags is low, especially on processors that are equipped with conditional branching prediction (i.e., all processors except those that are dedicated to small embedded systems), the technique known as *polymorphic inline cache* (PIC)\(^{12}\) is often preferred. The extracted tag is now tested for equality against each expected tag, and the right method is then statically called (Figure 5, right). The PIC sequences for a given method selector could be factorized in a common stub function. It would improve the code size, to the detriment of runtime efficiency, because branch prediction is markedly more accurate when PICs are inlined\(^5\).

The tag test can be saved on only in two specific situations: (i) when the receiver is self, and the enclosing class is either a specific primitive type, or a class without primitive subtypes; (ii) when the receiver is a literal or the direct

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\(^4\)In some cases where the test is useless, e.g., for monomorphic self-inocations (see hereafter), multiple entry points might save on this test, but this is a minor optimization since branch prediction would almost always succeed.
result of an instantiation (new). Therefore, apart from these situations, when
the invoked method is not known by primitive types the code sequence from
Figures 1 and 4 must begin with the following test:

\[
\text{cmp object, #0 } \quad \text{bgt #fail // unknown-method exception}
\]

In contrast, in common superclasses of primitives types and usual classes,
where \texttt{self} can be either a tagged value or a true object, method specialization
is a way to avoid a long code sequence.

\subsection*{2.2.3 null value}

Uninitialized variables and attributes represent a constant issue in object-oriented
programming. Initializing them with a distinguished null value is a first step,
but ubiquitous null-checks would be inefficient. In the dynamic typing context,
using 0 as the null value allows for integrating the null-check in the tag tests at
minor expense. \texttt{null} then implies the 0 tag. When the invocation site cannot
imply any primitive type, the prologue test has just to be replaced with the following:

\[
\text{cmp object, #0 } \quad \text{bge #fail // unknown-method or null exception}
\]

\section*{3 Compilation protocol}

As explained in the introduction, we are concerned, here, with dynamic compi-
lation. The compilation/recompilation protocol is in charge of a few tasks: (i)
computing the data structures associated with a class; (ii) generating machine-
code from method source code (or bytecode); (iii) recompiling some data struc-
tures and pieces of code when the assumptions supporting the previous compi-
lation are no longer valid.

The protocol relies on the principle that some part of the generated code
or structure is compatible with the \textit{open world assumption} (OWA), so that
it is computed in an incremental way, once for all. In contrast, another part
makes \textit{provisory close world assumptions} (CWA), which allows the compiler to
perform aggressive optimizations at the price of potential recompilations when
underlying assumptions are refuted.

In the recompilation protocol, we consider dynamic class loading, but exclude
class reloading.

\subsection*{3.1 Data structures}

The data structures associated with a class are twofold. Each time a class is
loaded, the runtime system builds a model of this class, ie an instance of some
metamodel (eg the metamodel proposed in [13]). This model links the newly
loaded class with its superclasses and the methods the class \textit{defines}, \textit{inherits}
or \textit{introduces}—these terms have intuitive meanings that are formalized in [13].
Methods that are introduced by several classes are of course distinguished, and
we call them \textit{overloaded}. This model is incremental and must be updated with
further class loadings, when a previously non-overloaded method becomes over-
loaded. Non-overloaded is thus a provisory, mutable feature, while overloaded
is definitive and immutable. Moreover, the model must maintain information about the liveness of classes and methods. A class is alive if it has been instantiated, while a method is alive if it has been already invoked. The definition may be slightly enlarged, by considering that compiling an instantation or invocation site is enough for making a class/method alive. Therefore, the overall protocol can be considered as a kind of static analysis like Rapide Type Analysis (RTA) [14], which is run very dynamically as the classes are loaded and the code is executed. In this situation, RTA is equivalent to Class Hierrarchy Analysis (CHA) [15]. Note, however, that this runtime analysis is different from profiling and remains static.

Class loading is triggered in two situations: (i) directly, when the considered class is instantiated (new); (ii) recursively, when a subclass is loaded. Methods tables are computed as soon as the considered class is instantiated, hence only in case (i), or when an already loaded class is instantiated for the first time. The method table itself is computed as in SST, but the hashtable is allocated in a lazy way, when it contains an overloaded method that is already alive, i.e., such that an invocation site has been already compiled. In the following, we will assume that this computation is done at class loading, but it could be postponed as well.

When the hashtable is computed, a perfect numbering algorithm is applied as follows. Let \( C \) be the considered class. Then \( M_C \) is the set of methods known by \( C \) that are introduced by several already-loaded classes. Some of these methods have already an ID, because they have been hashed in other classes. Let \( M'_C \) be this subset, and \( I'_C = \{ id_x \mid x \in M'_C \} \) be the corresponding set of method IDs. The other methods in \( M_C \) have no IDs, because they have not been hashed yet, and some subset \( M''_C \) must be hashed now. Note that the latter subset may include all the overloaded methods known by \( C \), or only those that are invoked in some methods that must be compiled or recompiled; this is a matter of tuning of the protocol. The respective cardinalities of these sets are denoted \( n'_C \) and \( n''_C \).

Perfect numbering is then applied with \( I'_C \) and \( n'_C \) as inputs; its outputs are a hash parameter \( H_C \) and a set of method IDs \( I''_C \), of cardinality \( n''_C \), which are assigned to the methods in \( M''_C \). A hashtable of size \( H_C \) is computed, filled and linked to the method table.

The algorithmic aspect is developed in Section 4.

3.2 Code generation

In the following, we consider only method invocation. Indeed, the case of subtype tests is similar to, though simpler than, method invocation and we don’t develop it. Besides, with SMALLTALK-encapsulation and single inheritance, attribute access is like with SST.

Method compilation is lazy, and it may be triggered by a trampoline in method- or hash-tables. When a method is compiled, each invocation site in the method body is compiled, and this is the main focus of the compilation protocol.

Specific cases. Firstly, different kinds of invocation sites can be distinguished from each other, and the distinctions can be static and hold under the OWA, or
dynamic and hold under a provisory CWA. The distinction concerns both the receiver and the invoked method.

- Statically, the receiver may be a literal; self, i.e., the current receiver which is statically typed by the enclosing class; or anything else.

- Dynamically, the receiver may be null, a tagged value or a standard object.

- Statically, the invoked method may be introduced by the hierarchy root.

- Provvisorily, the invoked method may be unknown, because the classes that introduce it have not been loaded yet, or it may be unknown in the static type of the receiver, in the specific case of self, or even in the receiver’s dynamic type when it can be statically inferred, e.g. for a literal or a direct instanciation (new).

- Provvisorily, the invoked method may be monomorphic, it can be introduced by a single class, or several ones.

- Provvisorily, the invoked method may be introduced by “standard” classes, i.e., classes without primitive subtypes, or not.

**Decision tree.** The complete combination gives the following decision tree whose conditions are tested sequentially (i.e., each condition implies the negation of the previous ones).

1. The receiver is a literal
   (a) the method is not known by the literal type → static type error;
   (b) otherwise → static call to the specific method (no test needed);

2. The receiver is self, and let C be the enclosing class, then
   (a) the method is unknown by all loaded subclasses of C → static call to the unknown-method function;
   (b) the method is known by C
      i. and not redefined in the subclasses of C → static call to the method inherited by C (no test)
      ii. otherwise
         A. C is a standard class → SST implementation
         B. C is a primitive type → PIC implementation
         C. otherwise → combined PIC-SST implementation
   (c) the method is introduced by a single subclass D of C,
      i. and not redefined in the subclasses of D → static call with a subtype test (depends on whether D is a standard class or a primitive type);
      ii. D is a standard class → SST implementation
      iii. D is a primitive type → PIC implementation
      iv. otherwise → combined PIC-SST implementation
the introduction subclasses are only standard classes \( \rightarrow \) PH implementation

\[\begin{align*}
\text{iii. otherwise } & \rightarrow \text{combined PIC-PH implementation}
\end{align*}\]

3. otherwise

\[\begin{align*}
(a) & \text{ the method is unknown } \rightarrow \text{static call to the unknown-method function;} \\
(b) & \text{the method is introduced by the root:} \\
& \text{i. it is currently monomorphic (not redefined in any class) } \rightarrow \text{static call;} \\
& \text{ii. it is not redefined in primitive types } \rightarrow \text{SST implementation;} \\
& \text{iii. otherwise } \rightarrow \text{PIC+SST implementation;} \\
(c) & \text{the method is introduced by a single class } D \Rightarrow (2-c); \\
(d) & \text{otherwise } \Rightarrow (2-d);
\end{align*}\]

3.3 Recompilation protocol

As mentioned above, the protocol is based on a model of the programs which involves an explicit representation of classes, method selectors and definitions, and method invocation sites, along with their relations to each other. Each method selector memorizes the method invocation sites compiled in a provisory way, so that the loading and compilation of a new definition for this method selector can trigger the recompilation of the concerned sites or enclosing methods.

Technically, recompilation can work at the method level, or at the invocation-site level. In the former case, the whole method is recompiled and the content of some method tables is updated. In the latter case, the invocation site is compiled into a stub function, called a thunk, and the original method code is modified in order to call it. A mixed approach should likely be preferred. All this is discussed in [7], but it remains out of the scope of this paper.

3.4 Optimization, laziness and efficiency assessment

The optimization problem is actually markedly more complicated than stated in Section 3.1, because several hash tables must be optimized at the same time (one for each class introducing a method in \( M_{C_i}^{C_j} \)), whereas what we called perfect numbering is intended to optimize a single hashtable. A more accurate formulation is presented in Section 4.

Efficiency assessment is characterized by three non-independent parameters, namely (i) the memory occupation, especially the hashtable size; (ii) the recompilation cost, for instance the number of hashtable allocations; (iii) the time-efficiency of the generated code. In the following, we will consider only the first two criteria, hashtable size and allocation number.

The overall protocol is essentially lazy, and laziness concerns not only allocation and computation time, but also computation content. Laziness should
have marked impact on the two efficiency criteria, but it might be in opposite directions.

- Hash tables should be allocated just-in-time, hence only when an overloaded method is invoked on an instance of the considered class. A simple way to do it is to initialize each method table with a common single-entry hashtable filled with a trampoline which will allocate the actual hashtable. This is the only point for which there is no doubt, and just-in-time allocation will be optimal on all criteria.

- The actual hashtable computation, which involves assigning IDs to method selectors, could occur at any time between the class loading and the hashtable allocation. From both the hashtable-size and compilation-cost standpoints, the effect of the computation time is unclear, because our actual optimizing algorithm iterates on method selectors in a non-optimal way.

- When a hashtable is computed/allocated, it may consider the whole set of known overloaded methods, or only those that have been considered alive, eg because an invocation site has already been compiled. If computation and allocation are restricted to live methods, the hashtable size will be optimized. However, it is likely that many hashtables will be recomputed and reallocated a number of times, each time a new overloaded method becomes alive, thus increasing the recompilation costs.

4 Problem definition and algorithms

4.1 Notations

Set and function notations. We mostly consider integers and integer sets. Notations are as usual. Function notations are extended to sets in the usual way, ie \( f(E) = \{ f(x) | x \in E \} \). Usual operations are extended to sets in a similar way, namely \( x + A = \{ x + y | y \in A \} \) and \( A + B = \{ x + y | x \in A, y \in B \} \).

Notations for bitwise representation. The hashing function we use is the bitwise and operation. Hence bitwise representation is essential, but it is often more intuitive to use instead a set-wise representation.

Let \( k \) be an integer number. Then, \( b(k) \) denotes the set of 1-bits of \( k \), ie the set of their positions in \( k \). Given two numbers \( j \) and \( k \), \( j \subset k \) is a shortcut for \( b(j) \subset b(k) \). Therefore a number can be represented as a set of number (its 1-bit positions) and can represent another set of numbers that have a subset of its 1-bits (however the notation will be \( j \subset b(k) \) instead of \( j \in b(k) \)). All these sets are of course finite, and \( b^{-1}(E) \) is defined for any finite set \( E \subset \mathbb{N} \).

In order to remove any ambiguity, given a set of numbers \( E \), \( |E| \) will denote the set of numbers whose 1-bit positions are included in \( E \), ie \( |E| = \{ n \in \mathbb{N} | b(n) \subset E \} \).

Moreover, \( \overline{k} \) represents the complement of \( k \), ie \( \overline{k} = \mathbb{N} \setminus b(k) \). Note that \( \overline{k} \) does not represent a number, because it is an infinite set. Given two numbers \( j \) and \( k \), \( j \subset \overline{k} \) is a shortcut for \( b(j) \subset \overline{k} \) (or, equivalently for \( j \in \overline{b(k)} \)). Other set notations will be used in a similar way.
Finally, we will use bitwise boolean operations or and and, with the usual logical notations $\lor$ and $\land$. For instance, $j \lor k = b^{-1}(b(j) \lor b(k)) = \lor(j,k)$.

4.2 Problem definition

4.2.1 Problem input

1. $X$ is a class set,

2. each class $x \in X$ is associated with a set $I_x$ of integers, and a set $S_x$ of symbols;

3. $p$ is a distinguished element in $X$, and $S_p = \bigcup_x S_x$;

4. for each $s \in S_p$, $\exists x \in X$ such that $x \neq p$ and $s \in S_x$.

5. finally, $F$ is a set of free identifiers, i.e $F \subset \mathbb{N}$ and $F \cap I_x = \emptyset$ for each $x \in X$.

Perfect hashing is applied to the $I_x$ sets with the bitwise and hashing function. Therefore, for each $x \in X$, $m_x = pha(I_x)$ is the least integer such that the function $y \mapsto (m_x,y)$ is injective on $I_x$. See [1, 4] for more details on this use of perfect hashing.

Context. $X$ is a set of classes in a dynamic typing and dynamic loading setting. $p$ is the class that is currently loaded and compiled in a lazy mode, and $S_p$ is the set of methods introduced by $p$ that are also introduced by other classes. Therefore, $X$ depends on $p$, and can be defined as the set of already loaded classes that introduce a method that is also introduced by $p$. Classes in $X$ are loaded but may not be compiled yet.

Note that there are definitely no constraints between $I_x$ sets, since they may as well be disjointed, overlap or contain each other. The same is true with $S_x$ sets, apart from points (3) and (4) above.

4.2.2 Problem output

The problem is to assign a unique identifier to each $x \in S_p$, more formally to define an injective function $id : S_p \rightarrow F$.

For each $x \in X$, let $m'_x$ be the resulting bitwise mask of $x$, i.e $m'_x = pha(I_x \cup id(S_x))$. Let $Y \subset X$, then $M(Y) = \sum_{x \in Y} m'_x$.

The assignment must be done in minimizing $m'_x$ on all classes. This can be specified in several ways:

1. minimizing $M(X)$;

2. minimizing first $m'_p$ then $M(X)$;

3. $X$ is partitioned in two subsets $X_1$ and $X_2$, with $p \in X_1$, and the optimization must first minimize $M(X_1)$, then $M(X_2)$.

The latter form is the more general.
Context. \(X_1\) is the set of classes that must be compiled now, whereas \(X_2\) is the set of classes whose compilation is not yet required. They differ in the fact that each class in \(X_1\) introduces a method that is multiply introduced and already called (i.e., a class site has been already compiled), whereas all the methods multiply introduced by classes in \(X_2\) are not yet called.

Therefore, the optimization criterion can be understood as minimizing the hashtable size that are currently computed while minimizing the tables that will likely be computed later.

4.2.3 Subproblems

The problem can be simplified in two ways: either \(X\) or \(S_p\) can be singletons.

\(S_p\) is a singleton \(p\) introduces a single method that is already introduced in several classes, and the problem is to assign a free identifier to this method that minimizes the hash masks of all classes in \(X\). This is a true instance of the problem considered, and it is certainly worthwhile to study this subproblem first. Although it would not be optimal, a heuristic solution to the general problem could be obtained with by iterating over \(S_p\).

\(X\) is a singleton When \(X\) is a singleton, this is no longer an instance of the problem considered, since the point (4) in the problem input is no longer verified. Nevertheless, this is an instance of the perfect numbering problem [4], and the solution is as follows. The capacity of \(m_p = pha(I_p)\) is defined as \(2^{\log(m_p)}\), and is the number of integers that \(m_p\) can discriminate. While the capacity is strictly less than the cardinality of \(I_p \cup S_p\), the least-weight 0-bit in \(m_p\) is switched to 1. This gives \(m'_p = pna(I_p, |S_p|)\). A subset \(I'_p\) of \(F\) is then computed in such a way that \(|\text{and}(m'_p, I'_p)| = |I'_p| = |S_p|\), and \(|\text{and}(m'_p, I_p)| \neq |\text{and}(m'_p, I_p)|\) and \(|\text{and}(m'_p, I_p)|\) are disjointed. Free IDs in \(I'_p\) are then removed from \(F\) and successively assigned to symbols in \(S_p\).

Perfect numbering can be simplified by considered that \(S_p\) is a singleton, too. Then \(m'_p = pna(I_p, 1)\), and \(f\) is defined as the least element in \(F\) such that \(\text{and}(m'_p, f) \notin \text{and}(m'_p, I_p)\). Iterating over \(S_p\) provides the same solution as above.

Perfect numbering is an essential step towards a solution of the problem considered.

4.3 Subproblem when \(S_p\) is a singleton

Subproblems may provide a better understanding of the general problem, along with a first, approximated solution.

4.3.1 Subproblem definition

The problem definition is simplified as follows.

Input

1. \(Y\) is a class set, with at least 2 members, and a distinguished element \(p\).
2. each class $x \in Y$ is associated with a set $I_x$ of integers;
3. a set of free identifiers $F \subset \mathbb{N} \setminus \bigcup_{x \in Y} I_x$.

Output the least free identifier $n \in F$ minimizing either $\sum_{x \in Y} \text{pha}(I_x \cup \{n\})$, or $\text{pha}(I_p \cup \{n\})$ then $\sum_{x \in Y} \text{pha}(I_x \cup \{n\})$.

4.3.2 Naive approach

A naive approach is an adaptation of perfect numbering to multiple masks, which would give the following algorithm.

**Algorithm 1**: Naive heuristics

Data: $F$, a free-identifier set; $s$, an overloaded method;
$Y$, a set of classes that know $s$, with masks $m_x = pna(I_x, 1)$ (all defined as above)

Result: a free-identifier in $F$ that can be assigned to $s$

```plaintext
free ← ⊥;
for each $f \in F$ until free ≠ ⊥ do
    ok ← true;
    foreach $x \in Y$ while ok do
        if $(f, m_x)$ is not free for $x$ then
            ok ← false;
        if ok then
            free ← $f$
    return free
```

The algorithm enumerates free identifiers and checks whether they are hashed in free places in the hash tables of all concerned classes (Algorithm 1). $m_x$ masks are assumed to be perfect hashing parameters for the method identifiers ($I_x$ set) already known by $x$, with an extra free place, hence $m_x = pna(I_x, 1)$. Therefore, free places can be formally defined as $\text{and}(m_x, \mathbb{N}) \setminus \text{and}(m_x, I_x)$.

When $Y$ is a singleton, this is exactly the perfect numbering algorithm, and there is always a solution if $F$ is large enough. However, in the general case, the algorithm works when there is a solution with the current masks, but it does not terminate when the problem is over-constrained, i.e., when the free places of all concerned classes are not compatible with each other. Therefore, a realistic heuristics should scan only a finite set in order to detect that there is no solution, then some hash tables would be enlarged. This finite set might be a subset of $F$, or some set that would be representative of $F$.

4.3.3 Problem analysis and transformation

**Bitwise and perfect hashing.** Let $x$ be a class in $Y$, and its associated hashable, which is isomorphic to the $0..m_x$ range, has size $m_x + 1$ and can be analyzed in the following way. First $b(m_x) \subseteq \bigcup_{i \in I_x} b(i) = b(\bigvee_{i \in I_x} i)$ (or, equivalently, $m_x \in \bigvee_{i \in I_x} [i]$). Moreover, $K_x = \text{and}(m_x, I_x)$ represents the set of occupied positions in the hashable, and $\forall k \in K_x, k = \text{and}(m_x, k)$. Thus, $K_x$ can be considered as a kind of representative of $I_x$. The complement of $K_x$ in the $0..m_x$ range is the set of unused entries, which can be partitioned...
into 2 subsets: \( U_x \) is the set of unreachable entries; \( A_x \) is the set of free entries that can be allocated for new identifiers. A position \( k \) in \( 0..m_x \) is said to be unreachable if \( k \not\in \text{and}(m_x, N) \). A necessary and sufficient condition is that \( b(k) \setminus b(m_x) \) is not empty. In contrast, a position \( k \) is reachable if \( b(k) \subseteq b(m_x) \), and a free position is a reachable position that is not occupied, hence not in \( K_x \). Alternatively, \( A_x = \text{and}(m_x, N) \setminus K_x \). Overall, \( 0..m_x = K_x \cup U_x \cup A_x \). An essential (though trivial) property is that the number of reachable positions is a function of the 1-bit count of the mask, namely \(|K_x \cup A_x| = 2^b(m_x)|\) [4].

**From free positions to free identifiers.** Part of the problem considered involves searching a free identifier that matches a free position in range \( 0..m_x \).

We could not imagine a constant-time technique that would return “the next free identifier that would fit the considered range”. Instead, we propose sequential algorithms. In any case, the set \( F \) of free identifiers can be implemented as a range union. Note that \( F \) is considered to be large enough to not be a problem constraint. Hereafter, we will make the informal assumption that, if there is an infinite series of numbers in \( N \) that satisfies a set of constraints, it is not disjointed from \( F \).

The first technique consists in allocating an explicit hash table of size \( m_x + 1 \), whose free entries are filled with a distinguished value. Then, for each free identifier \( f \in F \), the entry at position \( \text{and}(m_x, f) \) is checked. This is the technique used in our experiments of perfect numbering [4].

An alternative would involve enumerating \( k \in A_x \) and checking if there is a free identifier \( f \in F \) such that \( \text{and}(m_x, f) = k \). However, we could not imagine a data structure that would allow us to implement this inverse problem.

**From single class to multiple classes with mask-union.** As mentioned above, the degenerated case where \( Y \) is a singleton represents the perfect-numbering problem, which is solved. In the problem considered here, the allocation of free identifiers must take into account several classes that introduce common methods.

**Definition 4.1** Assuming the previous definitions for \( Y \), \( m_x \) and \( A_x \), a number \( k \in N \) is said to be PH-compatible with \( Y \) iff \( \forall x \in Y, \text{and}(m_x, k) \in A_x \).

The key idea relies on the following observation. Let \( x \in Y \) be a class, and \( m \) a mask such that \( b(m_x) \subset b(m) \). Let \( j \in 0..m \) be a position in \( m \) which is free for \( x \), ie \( \text{and}(j, m_x) \) is a free position for \( x \). Then any free identifier \( f \in F \) whose \( m \)-hash value is \( j \) will be free for \( x \) too, because of the bit-inclusion.

The idea can be generalized by considering the union of all masks implied, ie \( m = \bigvee_{x \in Y} m_x = b^{-1}(\bigcup_{x \in Y} b(m_x)) \). Then, the \( 0..m \) range is the finite set that must be considered. Indeed, if \( k \in 0..m \) is PH-compatible with \( Y \), then \( f \in F \) will verify the same property if \( \text{and}(m, f) = k \).

**Proposition 4.2** Let \( m = \bigvee_{x \in Y} m_x \) be the bit-union of all considered masks. Let \( f \in F \), and \( k = \text{and}(m, f) \).

Then \( k \) is PH-compatible with \( Y \) iff \( f \) is too.

Indeed, \( \text{and}(k, m_x) = \text{and}(\text{and}(f, m), m_x) = \text{and}(f, \text{and}(m, m_x)) = \text{and}(f, m_x) \). ☑
Algorithm 2: Realistic heuristics

Data: $F$, a free-identifier set; $s$, an overloaded method; $Y$, a set of classes that know $s$ (all defined as above)

Result: a free-identifier in $F$ that can be assigned to $s$

```plaintext
/* initialization */
mask ← 0;
for $x \in Y$ do
    mask ← or(mask, $m_x$)
frees ← ∅;
while frees = ∅ do
    foreach $j \subseteq$ mask do
        ok ← true;
        foreach $x \in Y$ while ok do
            if and($j, m_x$) is not free for $x$; /* failure */
                then
                    ok ← false;
                    if ok then
                        frees ← frees ⊎ $j$
        if frees = ∅ then
            select $x \in Y$ and enlarge $m_x$; /* enlarge some mask */
            update mask
    foreach $f \in F$ do
        if and($f, mask$) ∈ frees then
            return $f$
```

4.3.4 First Algorithm

Hence, a more realistic heuristics is Algorithm 2. This new algorithm is sound and terminates when $F$ is reasonably large enough and the selection of the enlarged mask is appropriate.

For instance, a simple scheme involves selecting the class $x \in Y$ that maximizes the number of failures (line 1), and minimizes the number of free places, and enlargement consists of switching the least-weight 0-bit in $m_x$.

Furthermore, when no enlargement is needed, this algorithm is optimal for the first subproblem, in that it can enumerate any identifier free in all classes in $Y$. This is a direct consequence of the equivalence in Proposition 4.2.

However, the overall algorithm is not optimal in several ways:

- enlarging the mask $m_x$ may lead to a solution at the next step, but enlarging another mask $m_y$ might have led to a better solution;
- enlarging the mask $m_x$ may be useless, because it does not lead to a solution at the next step and enlarging another mask $m_y$ is required anyway; moreover enlarging only $m_y$ would lead to a solution at the next step, but the selected identifier may occupy the enlarged $m_x$;
- as with perfect numbering, the selection of a free identifier is blindfold and never optimal for the future.
Algorithm 3: General algorithm

Data: $F$, a free-identifier set; $(X, \prec)$, a class hierarchy; $p$, a newly loaded class

/* initialization */

\[ p.mset \leftarrow \text{the set of methods that are introduced in } p \text{ and were already introduced in some unrelated class, but have no identifier yet}; \]

\[ X_p \leftarrow \text{the subset of classes in } X \text{ that have methods in } p.mset; \]

Result: Assigns a free identifier to each method in $p.mset$

\textbf{foreach} $x \in X_p$ \textbf{do}

\[ x.mset \leftarrow \text{the subset of methods in } p.mset \text{ known by } x \]

\[ x.iset \leftarrow \text{the set of method identifiers known by } x \]

\[ m_x \leftarrow \text{pna}(x.iset, |x.mset|); \] /* perfect numbering parameter */

\textbf{foreach} $s \in p.mset$ \textbf{do}

/* subproblem */

\[ Y_s \leftarrow \text{the subset of classes in } X_p \text{ that have } s; \]

\[ Y \leftarrow \emptyset; \] /* the $Y_s/\equiv$ quotient-set */

\[ \text{mask} \leftarrow 0; \]

\textbf{foreach} $x \in Y_s$ \textbf{do}

/* where $x \equiv y$ */

\[ \text{if } \exists y \in Y, \text{ such that } x \equiv y \]

then

\[ Y \leftarrow Y \cup x; \]

\[ \text{mask} \leftarrow \text{or}(\text{mask}, m_x) \]

\[ \text{frees} \leftarrow \emptyset; \]

\textbf{while} \text{frees} = \emptyset \textbf{do}

\textbf{foreach} $j \subset \text{mask}$ \textbf{do}

\[ \text{ok} \leftarrow \text{true}; \]

\textbf{foreach} $x \in Y$ \textbf{while} \text{ok} \textbf{do}

\[ \text{if and}(j, m_x) \text{ is not free for } x; \] /* failure */

then

\[ \text{ok} \leftarrow \text{false}; \]

\[ \text{if ok then} \]

\[ \text{frees} \leftarrow \text{frees} \cup j \]

\textbf{if} \text{frees} = \emptyset \textbf{then}

\[ \text{select } x \in Y \text{ and enlarge } m_x; \] /* enlarge some mask */

\[ \text{update mask} \]

\textbf{foreach} $f \in F$ \textbf{do}

\[ \text{if and}(f, \text{mask}) \in \text{frees then} \]

\[ s.id \leftarrow f; \]

\[ F \leftarrow F \setminus f; \]

\[ \text{foreach } x \in Y_s \text{ do} \]

\[ \text{update } x \]

\[ \text{break} \]
Algorithm 4: Mask enlargement

Data: mask and Y, as in Algorithm 3
Result: Switches the least-weight 0-bit in some masks in Y

failSet ← ∅; /* a set of sets of classes in Y */

minfailnb ← maxInt; /* common length of failSet members */

foreach j ∈ mask do
    fail(j) ← ∅;
    foreach x ∈ Y when (j, mx) is not free for x do
        fail(j) ← fail(j) ⊎ {x}
        nb ← |fail(j)|;
        if nb < minfailnb then
            minfailnb ← nb;
            failSet ← {fail(j)}
        else if nb = minfailnb then
            failSet ← failSet ⊎ {fail(j)}

bestFail ← heuristic selection in failSet;
foreach x ∈ bestFail do
    switch least-weight 0-bit in mx

4.3.5 Complete algorithm

We present now a complete algorithm which iterates over Sp and solves both difficulties (Algorithm 3). Let us consider first the initialization step. Each class x in Xp is initialized, with its sets of, respectively, method identifiers and new overloaded methods. Then, its mask mx is initialized by perfect numbering (line 1), i.e., the mask computed from the identifier set, is enlarged in a minimal way in order to be able to contain the new identifiers that have to be computed. In parallel, the quotient set Y/≡ is computed, with the equivalence relationship defined by the equality of both masks and identifier sets (line 2). It is worth noting that the equivalence relationship depends upon the method s considered.

With Algorithm 3, another source of non-optimality is that methods in Sp are not ordered in an optimal way. For instance, the algorithm can run on a method s1 which is under-constrained, and assign it a free place which would be essential for a method s2 which is over-constrained, but will be handled later. Furthermore, Algorithm 2 is rather simple but can be markedly slow because the Y class-set can be very large (hundred, or even thousands, of classes). It is thus essential to find an equivalence relationship that allows us to quotient the class set. Enlarging masks can represent another bottleneck. Indeed, it may occur that several masks must be enlarged at the same time and iterating single-mask enlargement may represent a bad solution because the enlarged masks are not correlated if they do not fail at the same places.

The mask-enlargement algorithm, invoked line 3 in Algorithm 3, involves several steps (Algorithm 4): (i) determining the places j that minimize the failure number, i.e., the cardinality of the set fail(j) = {x ∈ Y | and (j, mx) is not free for x}; (ii) among the places j minimizing |fail(j)|, determining the place k such that fail(k) minimizes some criterion, e.g., \( \sum_{x \in fail(k)} m_x \); (iii) enlarging all the masks in fail(k).
### Table 1: Statistics of all methods

<table>
<thead>
<tr>
<th>Method</th>
<th>Introduced total</th>
<th>Introduced μ</th>
<th>Introduced max</th>
<th>Defined total</th>
<th>Defined μ</th>
<th>Defined max</th>
<th>Inherited total</th>
<th>Inherited μ</th>
<th>Inherited max</th>
<th>Root</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>17774</td>
<td>9.1</td>
<td>164</td>
<td>23358</td>
<td>12.1</td>
<td>179</td>
<td>608494</td>
<td>311.1</td>
<td>544</td>
<td>160</td>
</tr>
<tr>
<td>digitalk3</td>
<td>13004</td>
<td>9.6</td>
<td>440</td>
<td>17104</td>
<td>12.6</td>
<td>460</td>
<td>613990</td>
<td>452.8</td>
<td>1065</td>
<td>324</td>
</tr>
<tr>
<td>digitalk2</td>
<td>5534</td>
<td>10.4</td>
<td>271</td>
<td>6858</td>
<td>12.8</td>
<td>272</td>
<td>154790</td>
<td>289.9</td>
<td>677</td>
<td>182</td>
</tr>
<tr>
<td>IBM-SF</td>
<td>25000</td>
<td>2.8</td>
<td>257</td>
<td>116152</td>
<td>13.2</td>
<td>320</td>
<td>384375</td>
<td>44.9</td>
<td>346</td>
<td>5</td>
</tr>
<tr>
<td>JDK1.3.1</td>
<td>9567</td>
<td>1.3</td>
<td>189</td>
<td>28693</td>
<td>3.9</td>
<td>150</td>
<td>142145</td>
<td>19.2</td>
<td>243</td>
<td>5</td>
</tr>
<tr>
<td>Java 1.6</td>
<td>22098</td>
<td>4.4</td>
<td>286</td>
<td>33351</td>
<td>7.0</td>
<td>291</td>
<td>180061</td>
<td>36.7</td>
<td>660</td>
<td>11</td>
</tr>
<tr>
<td>JDK 1.0.2</td>
<td>3100</td>
<td>5.3</td>
<td>75</td>
<td>5005</td>
<td>8.4</td>
<td>78</td>
<td>22365</td>
<td>37.0</td>
<td>158</td>
<td>12</td>
</tr>
<tr>
<td>Self</td>
<td>26267</td>
<td>14.6</td>
<td>233</td>
<td>29415</td>
<td>16.3</td>
<td>233</td>
<td>104015</td>
<td>577.4</td>
<td>960</td>
<td>1</td>
</tr>
<tr>
<td>Geode</td>
<td>8078</td>
<td>6.1</td>
<td>193</td>
<td>14214</td>
<td>10.8</td>
<td>207</td>
<td>305560</td>
<td>231.8</td>
<td>880</td>
<td>24</td>
</tr>
<tr>
<td>PRMcl</td>
<td>2369</td>
<td>4.9</td>
<td>115</td>
<td>3793</td>
<td>7.9</td>
<td>115</td>
<td>37517</td>
<td>78.3</td>
<td>208</td>
<td>29</td>
</tr>
<tr>
<td>Lov-eobjed</td>
<td>3631</td>
<td>8.3</td>
<td>117</td>
<td>5026</td>
<td>11.5</td>
<td>127</td>
<td>37436</td>
<td>85.9</td>
<td>289</td>
<td>24</td>
</tr>
<tr>
<td>SmartEiffel</td>
<td>4854</td>
<td>12.2</td>
<td>222</td>
<td>7865</td>
<td>19.8</td>
<td>222</td>
<td>53704</td>
<td>135.3</td>
<td>324</td>
<td>1</td>
</tr>
</tbody>
</table>

The table presents, successively, the statistics of method introduction, method definition, and method inheritance, and each group depicts the total number on all classes and the average (µ) and maximum value per class. The last column represents the number of methods introduced in the hierarchy root.

### 4.4 General problem

Without loss of generality, the problem is not pairwise, but instead involves a graph \((X, E)\) where two classes are related if they introduce common methods, ie \(E\) is formed of pairs \((x, y)\) such that \(S_x \cap S_y \neq \emptyset\). An alternative view is a bipartite graph \((X, S, E')\), where \(E'\) is formed of pairs \((x, m)\) such that \(m \in S_x\). The degree of each \(m \in S_x\) is at least 2. In both graphs, \(p\) is connected to all \(X_p\) (resp. \(S_p\)).

An optimal solution to this problem remains an open issue. However, as our experiments show, in the next Section, that Algorithm 3 gives acceptable results, searching for an optimal solution would mostly be for the sake of it.

### 5 Experiments and evaluation

Perfect method hashing and numbering has been tested, with different variants, on a variety of benchmarks similar to those used in previous articles, eg [1, 16, 17]. All experiments are done with random class-loading, as in [4].

#### 5.1 All-method perfect numbering

Table 1 presents the statistics on method definition in these benchmarks. The total of 'inherited methods' represents the memory occupation of method tables in the SST implementation (Figure 1), when it is relevant, or the positive part of method tables either in the MST implementation (Figure 2) or in the proposed implementation (Figure 4).

The next two tables present the results of perfect method numbering on these benchmarks, when classes are loaded with random leaf-class ordering. In Table 2(a) only methods are hashed, whereas classes are also hashed in Table 2(b). Therefore, the latter provides an implementation in multiple inheritance for both method invocation and subtype testing (Figure 3), and the latter must
### Table 2: Statistics of PN for all methods in dynamic typing

#### (a) Leaf-class ordering, single inheritance (without class IDs)

<table>
<thead>
<tr>
<th>Method</th>
<th>Optimal</th>
<th>Usefull PN</th>
<th>PN and SMI</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>min</td>
<td>μ</td>
</tr>
<tr>
<td>visualworks2</td>
<td>1.4</td>
<td>2.5</td>
<td>3.0</td>
</tr>
<tr>
<td>digitalk3</td>
<td>1.2</td>
<td>2.4</td>
<td>3.0</td>
</tr>
<tr>
<td>digitalk2</td>
<td>1.6</td>
<td>2.3</td>
<td>2.7</td>
</tr>
<tr>
<td>IBM-SF</td>
<td>1.4</td>
<td>3.6</td>
<td>4.5</td>
</tr>
<tr>
<td>JDK 1.3.1</td>
<td>1.4</td>
<td>2.6</td>
<td>3.3</td>
</tr>
<tr>
<td>Java 1.6</td>
<td>1.4</td>
<td>2.7</td>
<td>3.5</td>
</tr>
<tr>
<td>JDK 1.0.2</td>
<td>1.5</td>
<td>2.0</td>
<td>2.4</td>
</tr>
<tr>
<td>Self</td>
<td>1.6</td>
<td>2.8</td>
<td>3.6</td>
</tr>
<tr>
<td>Geoode</td>
<td>1.4</td>
<td>3.1</td>
<td>4.2</td>
</tr>
<tr>
<td>PRMcl</td>
<td>1.5</td>
<td>2.2</td>
<td>2.8</td>
</tr>
<tr>
<td>Lov-obj-ed</td>
<td>1.4</td>
<td>3.1</td>
<td>4.2</td>
</tr>
<tr>
<td>SmartEffel</td>
<td>1.4</td>
<td>2.5</td>
<td>3.2</td>
</tr>
</tbody>
</table>

All numbers are normalized w.r.t. the SST method-table size. The first column presents the theoretical optimum based on the PH-mask 1-bit count [4]. The next group depicts the random statistics of the useful part of PH tables, i.e., the entries that are reachable according to the bit-wise mask. The last group depicts the random statistics of PH tables. For the sake of comparison, the last column depicts the method-table size with the subobject-based implementation.

#### (b) Leaf-class ordering, multiple inheritance (with class IDs)

<table>
<thead>
<tr>
<th>Method</th>
<th>Optimal</th>
<th>Usefull PN</th>
<th>PN and SMI</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>min</td>
<td>μ</td>
</tr>
<tr>
<td>visualworks2</td>
<td>1.3</td>
<td>2.6</td>
<td>3.1</td>
</tr>
<tr>
<td>digitalk3</td>
<td>1.2</td>
<td>2.4</td>
<td>3.0</td>
</tr>
<tr>
<td>digitalk2</td>
<td>1.6</td>
<td>2.3</td>
<td>2.8</td>
</tr>
<tr>
<td>IBM-SF</td>
<td>1.4</td>
<td>3.5</td>
<td>4.7</td>
</tr>
<tr>
<td>JDK 1.3.1</td>
<td>1.4</td>
<td>2.7</td>
<td>3.3</td>
</tr>
<tr>
<td>Java 1.6</td>
<td>1.5</td>
<td>3.0</td>
<td>3.7</td>
</tr>
<tr>
<td>JDK 1.0.2</td>
<td>1.4</td>
<td>2.0</td>
<td>2.4</td>
</tr>
<tr>
<td>Self</td>
<td>1.6</td>
<td>3.0</td>
<td>4.0</td>
</tr>
<tr>
<td>Geoode</td>
<td>1.3</td>
<td>3.4</td>
<td>4.4</td>
</tr>
<tr>
<td>PRMcl</td>
<td>1.4</td>
<td>2.1</td>
<td>2.9</td>
</tr>
<tr>
<td>Lov-obj-ed</td>
<td>1.4</td>
<td>3.4</td>
<td>4.4</td>
</tr>
<tr>
<td>SmartEffel</td>
<td>1.4</td>
<td>2.6</td>
<td>3.2</td>
</tr>
</tbody>
</table>

In this table, methods and classes are hashed in the same hashtable, as needed for the implementation in Figure 3.

#### (c) All-class ordering, single inheritance

<table>
<thead>
<tr>
<th>Method</th>
<th>Optimal</th>
<th>Usefull PN</th>
<th>PN and SMI</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>min</td>
<td>μ</td>
</tr>
<tr>
<td>visualworks2</td>
<td>1.4</td>
<td>3.9</td>
<td>6.4</td>
</tr>
<tr>
<td>digitalk3</td>
<td>1.2</td>
<td>3.2</td>
<td>4.0</td>
</tr>
<tr>
<td>digitalk2</td>
<td>1.6</td>
<td>2.2</td>
<td>3.5</td>
</tr>
<tr>
<td>IBM-SF</td>
<td>1.4</td>
<td>6.0</td>
<td>7.6</td>
</tr>
<tr>
<td>JDK 1.3.1</td>
<td>1.4</td>
<td>4.3</td>
<td>7.6</td>
</tr>
<tr>
<td>Java 1.6</td>
<td>1.4</td>
<td>4.3</td>
<td>6.7</td>
</tr>
<tr>
<td>JDK 1.0.2</td>
<td>1.5</td>
<td>2.1</td>
<td>2.8</td>
</tr>
<tr>
<td>Self</td>
<td>1.6</td>
<td>3.0</td>
<td>3.8</td>
</tr>
<tr>
<td>Geoode</td>
<td>1.4</td>
<td>6.9</td>
<td>9.8</td>
</tr>
<tr>
<td>PRMcl</td>
<td>1.5</td>
<td>2.7</td>
<td>3.9</td>
</tr>
<tr>
<td>Lov-obj-ed</td>
<td>1.4</td>
<td>4.3</td>
<td>5.8</td>
</tr>
<tr>
<td>SmartEffel</td>
<td>1.4</td>
<td>2.9</td>
<td>3.6</td>
</tr>
</tbody>
</table>
Table 3: Statistics of methods with single or multiple introduction

(a) Single introduction

<table>
<thead>
<tr>
<th>Method</th>
<th>Introduced total</th>
<th>Introduced $\mu$</th>
<th>Introduced max</th>
<th>Inherited total</th>
<th>Inherited $\mu$</th>
<th>Inherited max</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>10465</td>
<td>5.4</td>
<td>153</td>
<td>506083</td>
<td>258.7</td>
<td>465</td>
</tr>
<tr>
<td>digitalk3</td>
<td>8577</td>
<td>6.3</td>
<td>328</td>
<td>559007</td>
<td>412.2</td>
<td>880</td>
</tr>
<tr>
<td>digitalk2</td>
<td>3902</td>
<td>7.3</td>
<td>215</td>
<td>139698</td>
<td>261.6</td>
<td>522</td>
</tr>
</tbody>
</table>

(b) Multiple introduction

<table>
<thead>
<tr>
<th>Method</th>
<th>Introduced number</th>
<th>Introduced total</th>
<th>Introduced $\mu$</th>
<th>Introduced max</th>
<th>All inherited total</th>
<th>All inherited $\mu$</th>
<th>All inherited max</th>
<th>Shared inherited total</th>
<th>Shared inherited $\mu$</th>
<th>Shared inherited max</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>2112</td>
<td>6235</td>
<td>3.2</td>
<td>108</td>
<td>72226</td>
<td>30.9</td>
<td>52014</td>
<td>27.1</td>
<td>181</td>
<td></td>
</tr>
<tr>
<td>digitalk3</td>
<td>1481</td>
<td>4427</td>
<td>3.3</td>
<td>114</td>
<td>54089</td>
<td>40.6</td>
<td>41544</td>
<td>30.6</td>
<td>255</td>
<td></td>
</tr>
<tr>
<td>digitalk2</td>
<td>585</td>
<td>1632</td>
<td>3.1</td>
<td>75</td>
<td>15098</td>
<td>28.3</td>
<td>10765</td>
<td>20.2</td>
<td>158</td>
<td></td>
</tr>
</tbody>
</table>

The first column depicts the number of method selectors that are introduced in several classes. The next two column groups are similar to that of previous tables, and the last one presents the statistics of inherited methods when sharing is taken into account.

be used in single inheritance, when subtype testing is implemented as in SST. The last column represents the method-table size in the C++-like subobject-based implementation of, multiple implementation (SMI), which can be considered as a negative reference as it is, in the worst-case, cubic in the number of classes, instead of quadratic as in most implementations [17]. All numbers are expressed as ratios to the SST implementation, i.e., the ‘total inherited’ column in Figure 1. Apart from the aforementioned impossibility to optimize this use of perfect hashing in an adaptive compiler, these results show that the memory requirement of this implementation is markedly higher than the SST implementation. The ratio to SST is more than 2 on average, and not markedly higher than the theoretical optimal (which is, however, not reachable, see [4]). It can exceed 7 on some benchmarks in the worst cases. Actually, in these worst cases (column max), it is not better than SMI.

Finally, Table 2(c) presents the same statistics as Table 2(a), but classes are now loaded with random all-class ordering. Interested readers are referred to [4] for an in-depth discussion about leaf-class vs all-class orders. Roughly speaking, the set of leaf-classes is close to the set of actual concrete classes, whose instantiation triggers class loading. Therefore, considering only leaf-class orders is closer to the expected behaviour of real programs, and PH/PN yields far better results with leaf-class orders.

5.2 Perfect numbering restricted to overloaded methods

The second experiment concerns class hierarchies in single inheritance, when methods are distinguished from each other according to the number of their introduction classes. The considered benchmarks are versions of SMALLTALK. Table 3 presents the statistics of method definition according to whether methods are introduced by a single class or several ones. They show that most methods are introduced by a single class. Hence, perfect hashing could considered because it would apply to a small subset of all method invocations, and the required memory requirement would remain low.
Table 4: Statistics of PN for overloaded methods, CWA heuristics

(a) Unshared

<table>
<thead>
<tr>
<th>Method</th>
<th>lashed</th>
<th>optimal</th>
<th>useful PN-and</th>
<th>PN-and</th>
<th>SST</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>36.9</td>
<td>55.2</td>
<td>124.1 197.2</td>
<td>615.2</td>
<td>1659.2</td>
</tr>
<tr>
<td>digitalk3</td>
<td>40.6</td>
<td>58.7</td>
<td>122.1 184.0</td>
<td>289.1</td>
<td>798.9</td>
</tr>
<tr>
<td>digitalk2</td>
<td>28.3</td>
<td>38.2</td>
<td>63.3 88.4</td>
<td>196.4</td>
<td>402.3</td>
</tr>
</tbody>
</table>

visualworks2    | 36.9   | 55.2    | 92.7 107.2    | 138.5  |           | 289.9 |
| digitalk3       | 40.6   | 58.7    | 106.1 128.5   | 149.0  |           | 452.8 |
| digitalk2       | 28.3   | 38.2    | 55.4 60.1     | 98.3   |           | 289.9 |

(b) Shared

<table>
<thead>
<tr>
<th>Method</th>
<th>lashed</th>
<th>optimal</th>
<th>useful PN-and</th>
<th>PN-and</th>
<th>SST</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>27.1</td>
<td>39.6</td>
<td>100.6 149.9</td>
<td>378.7</td>
<td>680.1</td>
</tr>
<tr>
<td>digitalk3</td>
<td>30.6</td>
<td>44.3</td>
<td>99.0 150.5</td>
<td>222.4</td>
<td>466.1</td>
</tr>
<tr>
<td>digitalk2</td>
<td>20.2</td>
<td>27.6</td>
<td>49.3 66.2</td>
<td>93.2</td>
<td>237.2</td>
</tr>
</tbody>
</table>

visualworks2    | 27.1   | 39.6    | 75.0 86.3     | 114.2  |           | 289.9 |
| digitalk3       | 30.6   | 44.3    | 88.5 108.1    | 127.0  |           | 452.8 |
| digitalk2       | 20.2   | 27.6    | 43.3 54.4     | 73.6   |           | 289.9 |

Each column presents the average number per class of, successively, the theoretical PH optimum, the observed random statistics of useful PH entries, and all PH entries. For the sake of comparison, the last column depicts the number of entries in the SST method table.

Each table is split into two subtables: the upper one presents the statistics on all class-loading orders, whereas the lower one restricts the statistics on leaf-class loading orders that are presumed to be closer to actual concrete-class loading.

The total extra size for the PH approach must be increased by two, for taking into account the extra indirection and hash parameter in the method table.

Finally, Table 4 presents the statistics of PN when it is restricted to overloaded methods. Benchmarks are a few Smalltalk class hierarchies. This approach is, however, not realistic in that it supposes that overloaded methods are identified under the closed world assumption. It is presented only in order to examine the efficiency of PN.

Results are now presented as an average size per class, instead of being a ratio to SST, as in Table 2. The last column recalls the SST average (column ‘inherited µ’ of Table 1). On average, the PH memory requirement is now lower than for SST and the overall worst-case ratio remains lower than 3.5. The space-efficiency is thus better than for all-method PH, and the time efficiency is even better since most PH invocations can be shortcut. However, in itself, the algorithmic efficiency is far lower. Indeed, in the worst cases of Table 2(a), all ratios are less than fivefold the optimal. In contrast, in Table 4(a), the worst cases are almost 20 times the optimal. This is simply explained by the fact that the optimal is reached with a single-inheritance hierarchy, when all methods are introduced by a single class [4]. Hence, methods with multiple introduction represent bad cases, and this approach considers only bad cases.
Table 5: Statistics of PH for overloaded methods with Algorithm 3

(a) Unshared

<table>
<thead>
<tr>
<th>Method</th>
<th>Unshared</th>
<th>Optimal</th>
<th>Useful PN- and</th>
<th>PN- and</th>
<th>SST</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>30.9</td>
<td>55.2</td>
<td>75.6</td>
<td>95.4</td>
<td>103.8</td>
</tr>
<tr>
<td>digitalk3</td>
<td>40.6</td>
<td>58.7</td>
<td>82.8</td>
<td>103.2</td>
<td>101.9</td>
</tr>
<tr>
<td>digitalk2</td>
<td>28.5</td>
<td>38.2</td>
<td>46.7</td>
<td>55.4</td>
<td>53.6</td>
</tr>
</tbody>
</table>

(b) Shared

<table>
<thead>
<tr>
<th>Method</th>
<th>Unshared</th>
<th>Optimal</th>
<th>Useful PN- and</th>
<th>PN- and</th>
<th>SST</th>
</tr>
</thead>
<tbody>
<tr>
<td>visualworks2</td>
<td>27.1</td>
<td>39.6</td>
<td>57.9</td>
<td>76.3</td>
<td>81.9</td>
</tr>
<tr>
<td>digitalk3</td>
<td>30.6</td>
<td>44.3</td>
<td>63.5</td>
<td>81.7</td>
<td>79.1</td>
</tr>
<tr>
<td>digitalk2</td>
<td>20.2</td>
<td>27.6</td>
<td>41.8</td>
<td>51.3</td>
<td>39.4</td>
</tr>
</tbody>
</table>

Space efficiency. Table 5 sums up our results with respect to the memory occupation required by the hash tables. Each subtable concerns, respectively, the cases where sharing is, or is not, taken into account. A subclass may share the hash table of its direct superclass, when the subclass does not introduce any proper overloaded method. Moreover, each subtable is in turn split into two subtables, for all-class and leaf-class orders, respectively.

For instance, the results for the visualworks2 benchmark, in the shared case with leaf-class loading (ie the lowest subtable in Table 5), can be read as follows. On average there are 27.1 methods per class that must be hashed, and it sets the average mask lower-bound to 39.6, based on the number of 1-bits required for hashing the considered methods. The ratio between these two numbers is always in range $1 \ldots 2$, and it represents the optimal occupation ratio (Proposition 3.7 in [4]). The PN-and columns present the statistics of the hash table size according to class loading orders. On average, it is about twofold the optimal and 3-fold the method number, and the deviation is rather small (about 10-20%). The useful PN-and columns presents the same statistics, restricted to the hashable entries that are actually reachable. The difference could be reused for allocating other data. Finally, for the sake of comparison, the last column presents the average size of the method tables, based on the SST implementation. It shows that hashables would represent only 30% of the method tables. Although not negligible, it remains quite reasonable in comparison with all-method PH or C++-like subobject-based implementation, both techniques whose ratio to SST

5.3 Experiments of Algorithm 3

Finally, we experimented Algorithm 3 on a few SMALLTALK class hierarchies, with random class-loading, as in [4].
Table 6: Statistics on iterations in Algorithm 3

(a) Iterations

<table>
<thead>
<tr>
<th>method</th>
<th>iteration number</th>
<th>over 6</th>
<th>max</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>0</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>visualworks2</td>
<td>2112</td>
<td>94.1</td>
<td>4.3</td>
</tr>
<tr>
<td>digitalk3</td>
<td>1481</td>
<td>91.2</td>
<td>4.2</td>
</tr>
<tr>
<td>digitalk2</td>
<td>585</td>
<td>91.5</td>
<td>5.6</td>
</tr>
<tr>
<td>visualworks2</td>
<td>2112</td>
<td>92.3</td>
<td>5.3</td>
</tr>
<tr>
<td>digitalk3</td>
<td>1481</td>
<td>91.0</td>
<td>5.9</td>
</tr>
<tr>
<td>digitalk2</td>
<td>585</td>
<td>89.2</td>
<td>7.0</td>
</tr>
</tbody>
</table>

(b) Fail number

<table>
<thead>
<tr>
<th>method</th>
<th>reiteration number</th>
<th>min failure number</th>
<th>over 6</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1</td>
<td>2</td>
<td>3</td>
</tr>
<tr>
<td>visualworks2</td>
<td>178</td>
<td>67.0</td>
<td>14.0</td>
</tr>
<tr>
<td>digitalk3</td>
<td>126</td>
<td>68.1</td>
<td>13.8</td>
</tr>
<tr>
<td>digitalk2</td>
<td>77</td>
<td>64.9</td>
<td>14.6</td>
</tr>
<tr>
<td>visualworks2</td>
<td>251</td>
<td>56.1</td>
<td>14.8</td>
</tr>
<tr>
<td>digitalk3</td>
<td>208</td>
<td>58.4</td>
<td>17.1</td>
</tr>
<tr>
<td>digitalk2</td>
<td>100</td>
<td>59.8</td>
<td>15.3</td>
</tr>
</tbody>
</table>

The first subtable depicts the statistics on the number of iterations required for hashing each overloaded method, and the first column is the number of overloaded methods. The second subtable depicts the statistics on the minimal number of failures, i.e., the number \( j \) that minimizes \(|\text{fail}(j)|\), at each iteration of the algorithm. The first column is the average reiteration number. Each column \( j \) represents the percentage of computations that, respectively, required \( j \) iterations, or presented at least \( j \) failures. The last column represents the maximum observed value for \( j \).

can exceed 6 (Table 2).

**Runtime-efficiency of the algorithm.** Regarding the runtime-efficiency of the algorithm, it appears that it is not that fast, and markedly slower than our previous applications of perfect hashing. Therefore, a careful implementation seems to be essential.

Table 6 presents some statistics about the behaviour of the algorithm which is rather satisfactory. Table 6(a) shows that there are few (less than 10%) enlargement cases (i.e., line 3 in Algorithm 3) and, most of the times, a single iteration is sufficient. In very exceptional cases (less than 1%), more than 5 iterations are required. The overall number of reiterations is about 12-18% of the method number. Table 6(b) presents statistics on the minimum failure number, i.e., the minimum number of masks that must be enlarged, for each iteration. In contrast with the iteration number which is rather low, when the current masks are not compatible with an extra identifier, the minimum number of failures is 1 in more than 50% of cases, but it may be greater than 20 in a very few cases.

**Recomputation cost.** The recomputation cost is less easy to assess, because our simulation is eager, and hashtable-recomputation count is markedly exaggerated in comparison with a lazy behaviour. Anyway, Table 7 presents statistics on different parameters. The first column group represents the count of all
hashable computation and recomputations, when sharing is taken into account, but not the fact that the hashtablen size may remain unchanged in the computation. The second group represents the count of recomputations that involve an enlargement of the considered hashtables. Thus it is the exact number of hashtables that would be allocated during the loading of all classes. The third group presents different class numbers: classes that (i) introduce or (ii) know overloaded methods, then (iii) all classes. The former is exactly the number of shared non-empty hashtables that are required when all of the classes have been loaded. In contrast, the number of shared recomputation represent the number of dynamic allocations of a hashtable which is larger than the previous one. Implicitly, each class is initialized with a common empty hashtable, i.e. a hashtable with a single empty entry. Therefore, the test shows that the number of allocations is higher than what is required, in a ratio less than 2 on average. Finally the number of all shared recomputations is far higher, but they mostly involve updating an existing hashtable which does not require to be enlarged, and the extra cost only implies a few assignments.

For instance, with the visualworks2 benchmark there are 1066 classes introducing overloaded methods. On average, with leaf-class ordering (lower subtable), 6697 hashtable updates are needed, including 1796 allocations.

**Leaf-class vs all-class orders.** Finally, it is also worth comparing the results and behaviour of Algorithm 3 with respect to leaf-class vs all-class orderings, although they represent an alternative in the simulation, not in an actual execution which should be midway between.

Leaf-classes provide far better results in terms of hashtable size (Table 5), but they are indeed more costly in terms of iterations and failures (Table 6), and for recomputations (Table 7).

### 6 Related Works, Conclusions and Prospects

#### 6.1 Related works

Smalltalk and Self have been pioneers of object-oriented implementation and compilation in a dynamic typing and dynamic loading setting. In this context, all the techniques that we are aware of rely on **inline caches** [18, 19, 12]. An inline cache can be viewed as a guarded monomorphic call. The receiver's
dynamic type is compared to an expected type, and a success yields a static call. In case of failure, an obscure process called lookup is performed, and looks up in the class hierarchy for the method that must be invoked.

There are a variety of inline caches, and they can be static or dynamic, mono- or poly-morphic. While a static cache\(^5\) is immutable and results from static compilation, dynamic caches are mutable and result from the runtime behaviour of the program; for instance, a cache miss yields the update of the cache with the lookup result; runtime profiling is also possible. Polymorphic caches involve more than one expected type. Overall, inline caches present pros and cons. When their guard succeeds, they are very efficient because of conditional-branching prediction of modern universal processors. Hence, in the best cases, inline caches are almost as efficient as static calls. These best cases include the cases where the receiver is monomorphic (see footnote 2). When the invocation, instead of the receiver, is monomorphic, the guard must access the method table in order to compare the invoked method with the expected one. This is mainly used for inlining.

What about bad cases? In practice, the failure case, ie the famous lookup, is inefficient. Indeed, we are not aware of any constant-time technique available in dynamic typing apart from perfect hashing. Moreover, a cache might be efficient at a moment, not during the whole program execution. For instance, in the program prologue, an invocation site may have receivers of type \(A\), then receivers of type \(B\) during the rest of the execution. If the site is optimized according to the prolog it will be unoptimized for the main part of the execution. Polymorphic caches are a solution, but it is always possible to build a bad case from any good situation. Another solution involves dynamic caches, but runtime profiling is so costly that the solution might be worse than the problem. Inline caches also yield longer code sequence, which increase the overall code size. While the thunk approach is still possible, it cannot be envisaged to factorize the same thunk between several similar invocation sites because the prediction of conditional branching would lose its accuracy.

In contrast, our proposal does not involve any of the drawbacks of inline caches, and it provides better solution for some of their best aspects, eg for monomorphic invocations instead of monomorphic receivers. The worst case of our proposal, ie perfect hashing, is likely more efficient than the lookup, although the latter might use the former. Furthermore, it remains possible to couple inline caches with the less efficient sequences generated according to our proposal, which are not so many.

Distinguishing between overloaded and non-overloaded methods was also proposed in [20], however in the context of global, static compilation.

### 6.2 Conclusion and prospects

In this paper, we proposed a novel object representation for method dispatch in dynamic typing, single inheritance and dynamic loading. To our knowledge, this is the first constant-time implementation of method dispatch in this context that requires reasonable memory occupation.

This technique involves hashing overloaded methods, and its efficiency relies on the fact that they are not too many.

---

\(^5\)A static cache looks like an oxymoron.
Simulation over a few SMALLTALK benchmarks show that the technique is promising. Indeed, the number of overloaded methods is actually low, and the overall hashtable size remains reasonable. However, the simulation we carried out is unable to assess the recompilation cost, because it is markedly more eager than would be an actual compiler. Hence the cost that we observed are exaggerated.

Therefore, the main prospects of this work is to perform simulations that would be closer to actual executions, for instance by adapting to dynamic typing the simulations used in [7]. It would be worth considering, too, the possibility of simulating these implementation and recompilation protocol in a SMALLTALK virtual machine via meta-programming.

References

[8] A. Goldberg and D. Robson. Smalltalk-80, the Language and its Implementation. Addison-Wesley, Reading (MA), USA, 1983.


