Face-to-Face Meeting

Inria Rennes Campus de Beaulieu Rennes March 29-30, 2016



Design Continuum for Next Generation Energy-Efficient Compute Nodes

Agenda Tuesday, March 29th	
09:30	Agenda presentation and objectives of the meeting – Coordinator
09:40	 Implementation of heterogeneous multicore systems with Cortus technology (Cortus) SystemC and FPGA oriented designs Early feedback on gem5 experience
11:00	Coffee Break
11:15	 Emerging memory and communication technologies (LIRMM) Quick introduction to non volatile memories Quick introduction to Networks-on-Chip
12:30	Lunch
14:00	A design exploration flow for multicore architectures integrating non volatile memories (LIRMM)
15:00	Design of relevant metrics for performance and energy efficiency (Inria Rennes)
16:00	Coffee Break
16:15	Introduction to potential static code optimizations as a preliminary step in CONTINUUM (Inria Rennes)
17:15	Ajourn
Agenda Wednesday, March 30th	
09:30	 Technical discussion (AII) Benchmarks and applications for CONTINUUM approach validation? Which experimental platforms: gem5, SystemC, Zync board?
11:00	Coffee Break
11:15	 Further discussion (AII) Grant agreement Next meeting
12:00	Lunch & closing