Face-to-Face Meeting

LIRMM

Batiment 5 - 860 rue de St Priest
34095 Montpellier cedex 5
October 18 - 19, 2018

<td colspan="2" align="center">**Thursday, October 18th**</td>	
09:30	Introduction -- **Coordinator** ▪ Agenda and objectives of the meeting ▪ Project progress by WP, review of deliverables, milestones
09:45	On NVM integration in systems **– LIRMM and Inria Rennes** ▪ Last results on energy-efficiency evaluation for NVMs at main memory level ▪ Memory Mapping based on Partial WCET Analysis and Variable Retention-Time NVMs
11:00	**Coffee Break**
11:15	On NVM integration in systems **– LIRMM** ▪ Leveraging cache replacement policies in presence of NVM in cache memory hierarchy
12:30	**Lunch**
14:00	Current status on the design of the Continuum prototype architecture V2 **-- Cortus** ▪ Basic principles ▪ Live demo Assessment of the Continuum prototype architecture V1 **-- LIRMM** ▪ Performance and power consumption evaluation ▪ Thoughts on application benchmark implementation
16:00	**Coffee Break**
16:15	Technical discussion on gained insights w.r.t. Continuum prototype **-- All**
17:15	**Ajourn**
<td colspan="2" align="center">**Dinner**</td>	
<td colspan="2" align="center">**Friday, October 19th**</td>	
09:30	Approach under investigation for heterogeneous architecture assessment **– LIRMM and Inria Rennes** ▪ Illustration of system reconfiguration due to varying conditions ▪ Ongoing investigations on workload management in heterogeneous architectures ▪ A compiler-centric infra-structure for whole-board energy measurement on heterogeneous Android systems
11:00	**Coffee Break**
11:15	Some considerations about compiler optimization assessment **– LIRMM** ▪ The case of invisible instructions
12:00	**Lunch**
14:00	Strategic discussion **-- Coordinator** ▪ Review of project roadmap ▪ Current obstacles and actions to be taken ▪ Dissemination plan: announcements, papers, etc. ▪ Next meeting
15:00	Peer-to-peer discussion between partners **-- All**
17:00	**Closing**