

DDECS

DESIGN & DIAGNOSTICS of ELECTRONIC CIRCUITS & SYSTEMS



April 24-26, 2019, Cluj-Napoca, Romania

Call for Papers

General Chairs:

Liviu Miclea

György Cserey

Program Chairs:

Alberto Bosio

Zoran Stamenkovic

Topic Chairs:

Elena-Ioana Vatajelu

Petr Fišer

Giorgio Di Natale

Paolo Bernardi

Muhammad Shafique

Goran Stojanovic

Key Dates:

Submission Deadline:

14/01/2019

Notification of Acceptance:

15/03/2019

Camera Ready:

01/04/2019

Aim of the Conference: The International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test, and diagnosis of microelectronic circuits and systems.

DDECS'19 covers the areas of design and testing of electronic components, both digital and analog. The topics include the following but are not limited to:

- Modeling, circuit design and test for computation structures based on emerging technologies
- Design, test and reliability of logic structures based on emerging technologies
- Embedded Systems
- Dependable HW / SW Systems
- On-line Testing
- Memory, Processor Testing
- MEMS Testing
- Physical Design
- Cyber Physical Systems
- Industry 4.0
- Embedded Machine Learning
- Self-Aware Systems Design and Test
- Unconventional Computing Paradigms
- Internet-of-Things Design and Test
- Case Studies and Applications
- Functional Safety
- Reliability testing
- SoC and NoC Design and Test
- ASIC/FPGA Design
- Built-in Self-Test and Self-Repair
- Bio-Inspired Hardware
- Design Verification/Validation
- Formal Methods in System Design
- Hardware/Software Co-Design
- IP-based Design
- Logic Synthesis
- Defect/Fault Tolerance and Reliability
- Design and Test in Nano-Technologies
- Analog, Mixed-Signal, RF Design and Test
- ATE Hardware and Software
- Design for Testability and Diagnosis
- Modeling, circuit design and test for emerging technologies-based data
- Emerging technologies-based logic design, test and reliability storage/memory
- Security Primitives
- Hardware Security and Trust
- Hardware Security and Emerging Technologies
- Hardware Trojans



Papers Submission: Authors are invited to submit original, unpublished research work to DDECS'19. Paper submissions should be complete manuscripts. The paper must clearly indicate the area, main results and contributions. The manuscript should follow the instructions included in the author's kit at the conference web site. Submitted articles should be in PDF format and uploaded to the on-line submission system. Submissions will be considered as evidence that, upon acceptance, the corresponding author assumes responsibility of preparing the final version in due time and, also, the commitment of registration and presentation of the paper at the conference. Accepted papers will be considered for submission to the IEEE Explore.

Visit the DDECS'19 website for more information: <http://www.lirmm.fr/DDECS2019>