

CALL FOR PARTICIPATION

IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

ISVLSI
2008

April 7-9 2008
Montpellier, France



This Symposium explores emerging trends and novel ideas and concepts in the area of VLSI. The Symposium covers a range of topics: from VLSI circuits, systems, and design methods to system level design and system-on-chip issues, to bringing VLSI experience to new areas and technologies. Future design methodologies will also be one of the key topics at the workshop, as well as new CAD tools to support them. Over almost two decades the Symposium has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI. The Symposium is bringing together leading scientists and researchers from academia and industry.

This Symposium has a very high quality, and we are determined to keep a strong emphasis on this critical aspect of any conference. The Symposium proceedings are published by IEEE Computer Society Press. This Symposium has established a reputation in bringing well-known international scientists as invited speakers, and this trend will continue.

The ISVLSI 2008 is organized by the LIRMM (Montpellier - France).

Selected topics

- Emerging Trends in VLSI, Nanoelectronics Molecular, Biological and Quantum
- Computing, MEMS, VLSI Circuits and Systems, System Level Design,
- Field-programmable & Reconfigurable Systems System-on-a-Chip Design,
- Application-Specific Low Power VLSI System Design, System Issues in Complexity,
- Low Power, Heat Dissipation, Power Awareness in VLSI Design Test and Verification,
- Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, Physical Design,
- Intellectual property creating and sharing.

The Symposium Program will include contributed papers, invited speakers as well as 2 poster sessions.

Registration

Early registration deadline: March 15, 2008

Please, visit our website to register online:

<http://www.lirmm.fr/isvlsi2008/registration/registration.php>

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Website:

www.lirmm.fr/isvlsi2008

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LIRMM, Montpellier, France

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University of Montpellier/CNRS

