



# Side-channel Analysis of Cryptographic Implementations

Evaluation & Counter-Measures

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UNIVERSITÉ DE  
MONTPELLIER

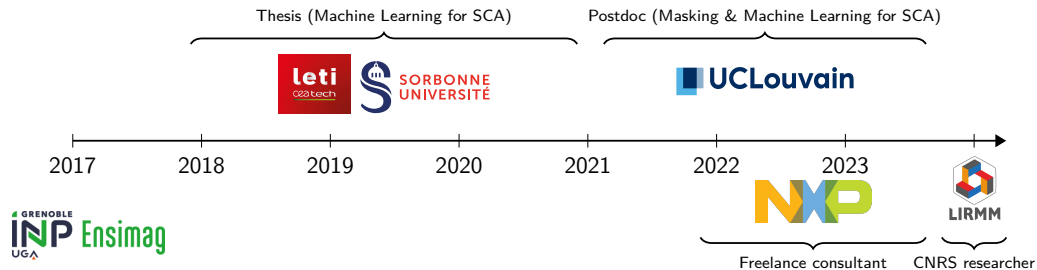
# Acknowledgements

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This work received funding from the France 2030 program, managed by the French National Research Agency under grant agreement No. ANR-22-PETQ-0008 PQ-TLS



# Who am I?



# Agenda

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## Introduction: SCA

## The Core Problem: Make & Certify a Device as Secure

- Security Certification

- Deep Learning Attacks

- Use Case: Polymorphic Implementation

- More Evaluation Shortcuts

## Masking

- Security Analysis for a Single Encoding

- Computing on Masked Secrets

- Security Analysis over Computations

## What about Post-Quantum?



# Content

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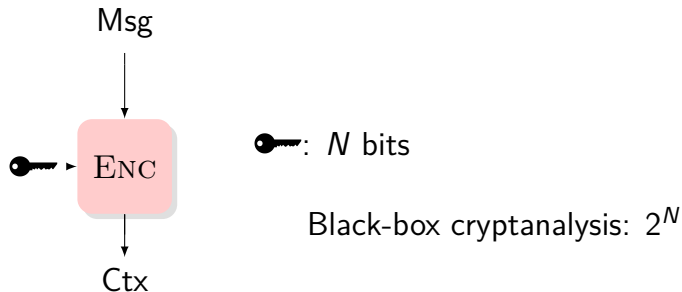
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# Context : Side-Channel Analysis (SCA)

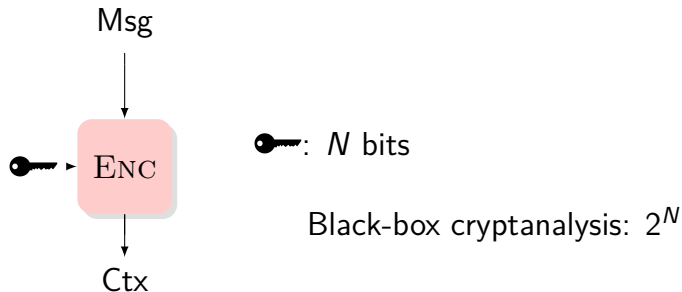
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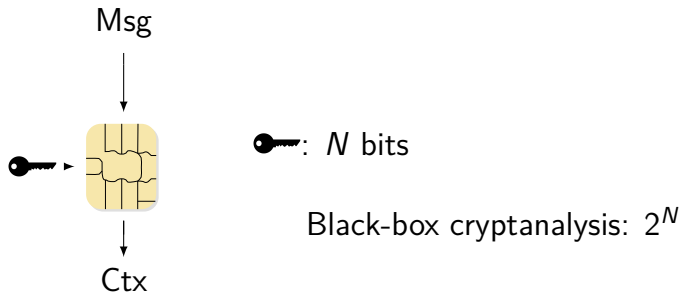
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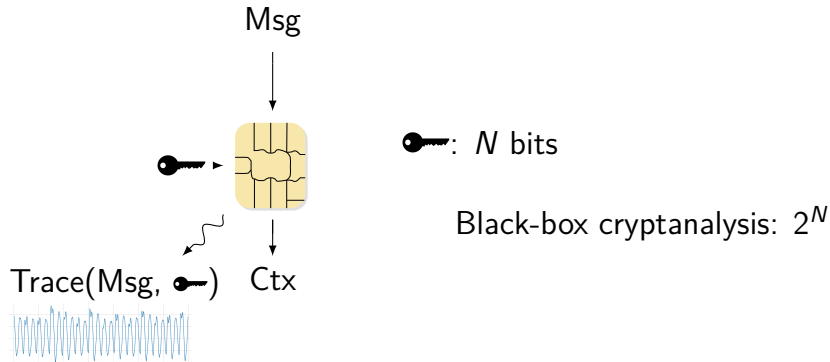
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# Context : Side-Channel Analysis (SCA)

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# Side Channel = Unintended Communication Channel

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## Example: the Washington Pizza Index<sup>1</sup>

NEWS

### CRUSTY D.C. VETERAN SAYS WAR IS NEAR

By Cox News Service  
Chicago Tribune • Published: Jan 16, 1991 at 12:00 am



WASHINGTON — The pizza index indicates military action is imminent in the Persian Gulf, a Domino's delivery official said Tuesday.

Record numbers of late-night pizzas have been delivered this week to the White House, Pentagon and State Department, said Frank Meeks, owner of several Washington-area Domino's outlets.

Similar order patterns came immediately before the invasions of Panama and Grenada, Meeks said.

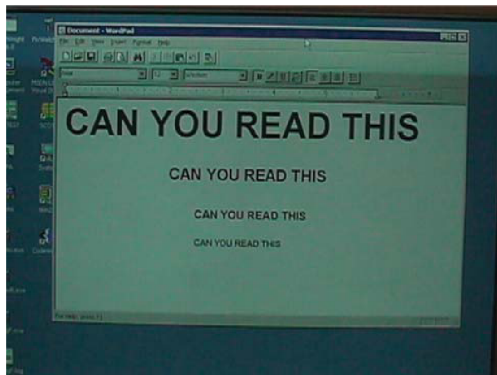
The increase in pizza orders at key government buildings after 10 p.m. is "very unusual," Meeks said. "I don't think they're sitting around watching Redskins reruns."

**Figure:** Chicago Tribune, **Jan. 16 1991**, the day before *Desert Storm* operation began.

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<sup>1</sup>Reality questioned: <http://home.xnet.com/~warinner/pizzacites.html>

# What is a Side Channel? A First Example



(a) A good old monitor



(b) Reconstruction from EM field

Figure: An example from Koç, *Cryptographic Engineering*.

# An Exemplary SCA on Crypto

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RSA: Modular exponentiation over large ( $\approx 2000$ -bit wide) integers



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Op.	Guess
$\times M$	
square	
square	
square	
$\times M$	
square	
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square	

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$\times M$ square square square	$k_N = 1$
$\times M$ square	
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Op.	Guess
$\times M$ square	$k_N = 1$
square square	$k_{N-1} = 0$
$\times M$ square	
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$\times M$ square	



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$\times M$ square	$k_{N-4} = 1$

# Can you guess the key from the Oscilloscope?

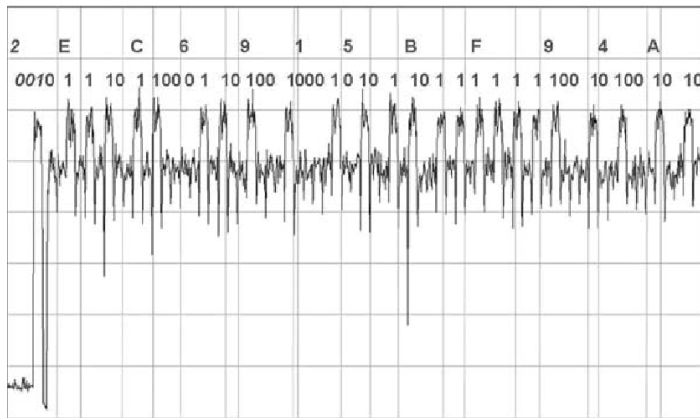


Figure: Power consumption. Illustration from Koç, *Cryptographic Engineering*

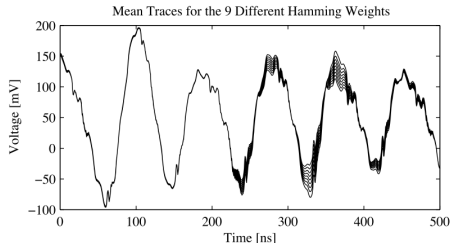
# Power Analysis on Symmetric Key

Power consumption: each bit  $x_i$  of a data chunk  $X$  stored in a register<sup>2</sup>

$x_i = 0 \implies$  register voltage = 0

$x_i = 1 \implies$  register voltage  $\neq 0$

Overall consumption of  $X$  is  
proportional to  $\text{hw}(X) = \sum_i x_i$   
hw = Hamming Weight



<sup>2</sup>Mangard, Oswald, and Popp, *Power analysis attacks - revealing the secrets of smart cards*.

# Practical Attack on AES, with Correlation

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In practice:  $L_P \propto \text{hw}(k \oplus P) + \mathcal{N}(0, \sigma^2)$

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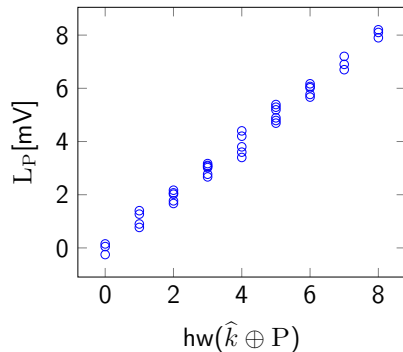
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Distinguisher with a statistical test: for all key hypothesis  $\hat{k} = 0, 1, 2, \dots$

If  $\hat{k} = k^*$ , then  $L_P$  should be highly correlated with  $\text{hw}(\hat{k} \oplus P)$

$$\rho_{\hat{k}} = \frac{\text{Cov}_{P, \mathcal{N}}(L_P, \text{hw}(\hat{k} \oplus P))}{\sqrt{\text{Var}_{P, \mathcal{N}}(L_P)} \cdot \sqrt{\text{Var}_{P, \mathcal{N}}(\text{hw}(\hat{k} \oplus P))}} \approx \pm 1$$



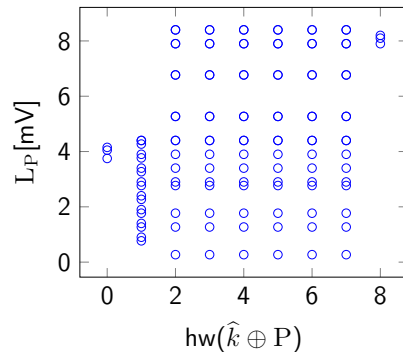
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If  $\hat{k} \neq k^*$ , then  $L_P$  should be poorly correlated with  $\text{hw}(\hat{k} \oplus P)$

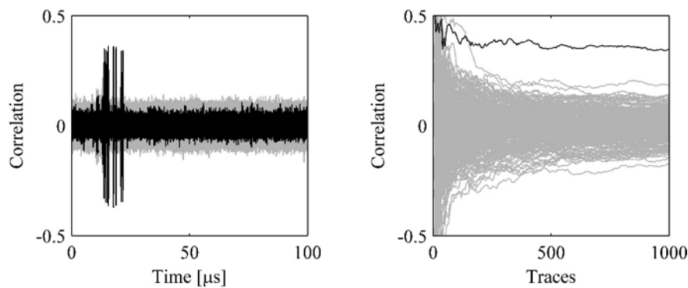
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# Power Analysis on Symmetric Key

Power consumption: each bit  $x_i$  of a data chunk  $X$  stored in a register<sup>3</sup>

Key guessed by a statistical test leveraging the correlation between the Hamming weight of data and the power consumption



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# It's Demo Time

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Application of the Correlation Attack on a ChipWhisperer  
CW = Target device (8-bit MCU) + Oscilloscope

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- **Q1:** What is the attack complexity ?

One  $n$ -bit key chunk:  $\mathcal{O}(2^n)$

$N$ -bit full key: divide-and-conquer  $\implies \mathcal{O}\left(\frac{N}{n} \cdot 2^n\right) \approx$  “quantum” break

# Counter-Measure

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**Q2:** Can you find a simple counter-measure for this attack ?

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<sup>4</sup>Rivain, Prouff, and Doget, “Higher-Order Masking and Shuffling for Software Implementations of Block Ciphers”.

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→ Shuffling  $t$  independent operations (e.g., ARK or SubBytes)<sup>4</sup> or inserting  $t$  dummy operations<sup>5</sup>

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- What can the adversary do? *Integrated* attack:  $\rho_{\text{shuffling,integrated}} = \frac{\rho}{\sqrt{t}}$

---

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# Contradicting Goals

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For correlation attacks, we usually target AddRoundKey or the SubBytes (bijective between each other)

- **Q3:** What's the “best” target ?

- When targeting  $\text{hw}(k \oplus P)$ ,  $k^*$  and  $\overline{k^*}$  undistinguishable: *ghost* peaks

- Problem solved when targeting  $\text{hw}(\text{Sbox}[k \oplus P])$

Contradicting goal: Sbox brings confusion to thwart cryptanalysis, but helps side-channel analysis<sup>6</sup>

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<sup>6</sup>Prouff, “DPA Attacks and S-Boxes”.

# Content

## Introduction: SCA

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### The Core Problem: Make & Certify a Device as Secure

- Security Certification

- Deep Learning Attacks

- Use Case: Polymorphic Implementation

- More Evaluation Shortcuts

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- Security Analysis for a Single Encoding

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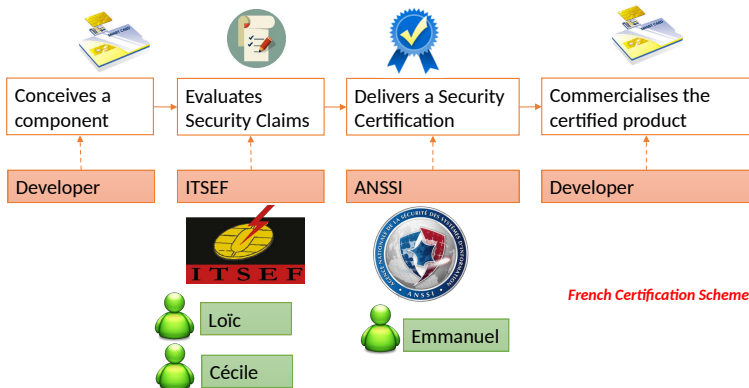
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## Perspectives

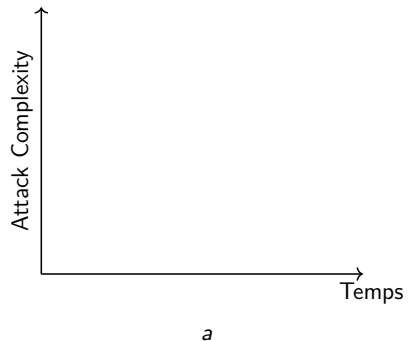
# Certification against SCA



Security graded w.r.t. attack complexity in terms of human, material, and financial means

# Evaluate Security against Side-Channel Attacks

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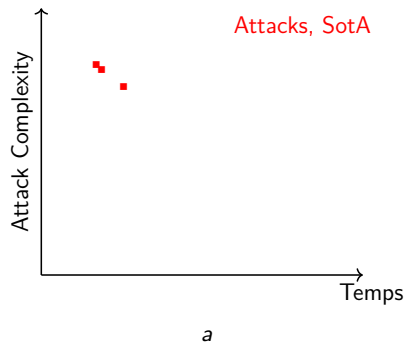


*Attack* approach (industry):

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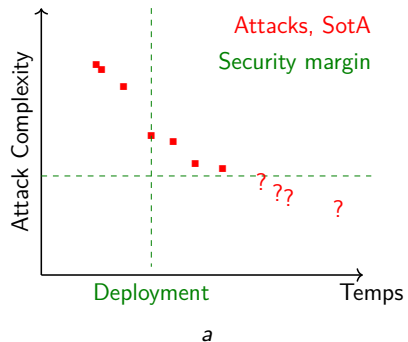


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Current security level ✓

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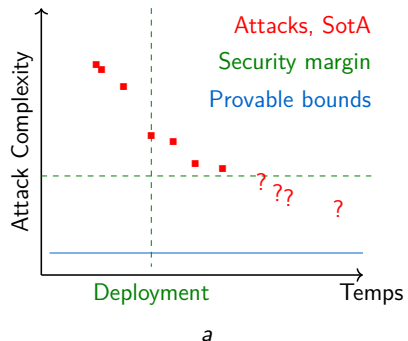
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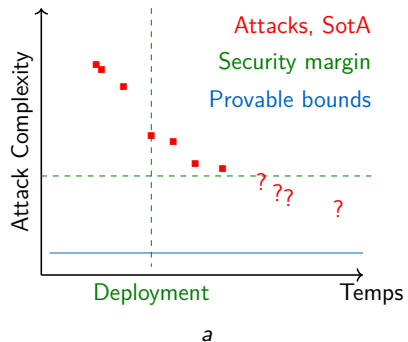
Approach by *proofs* (academia):

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Potentially conservative ✗

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Agenda: evaluation by attack (today), evaluation by proofs (tomorrow)

# How to Evaluate Efficiently? Interlude

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*A good evaluator  $\mathcal{E} \neq$  A good adversary  $\mathcal{A}$*

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<sup>7</sup>Analogy with real estate

# How to Evaluate Efficiently? Interlude

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A good *evaluator*  $\mathcal{E} \neq$  A good *adversary*  $\mathcal{A}$

Evaluating = guessing how much the best  $\mathcal{A}$  must pay to succeed<sup>7</sup>

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  - Characterize analytically the *best*  $\mathcal{A}$
  - Decompose each attack step
  - Quantify the complexity of each step

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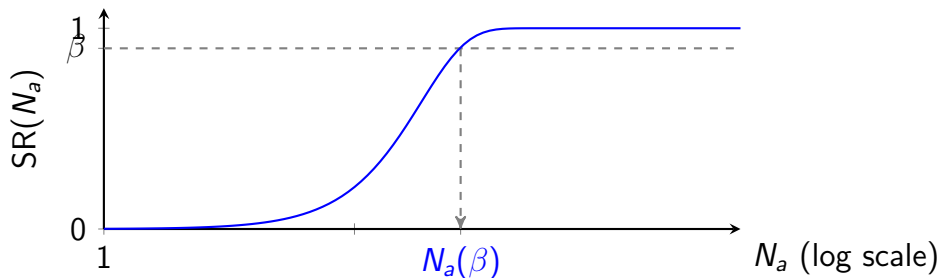
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⇒ Finding evaluation shortcuts

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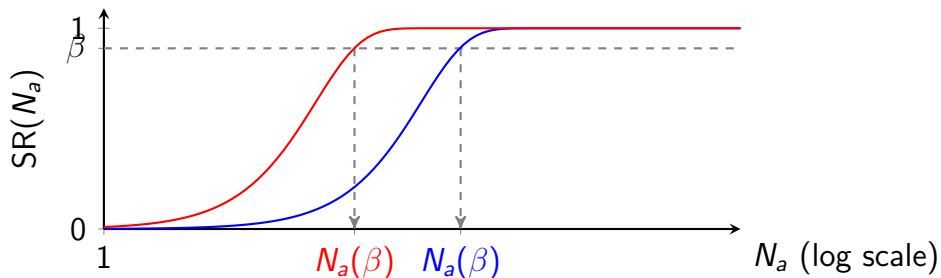
# Assessing an Attack: the Success Rate<sup>8</sup>



- SR: probability to succeed the attack within  $N_a$  queries to the target

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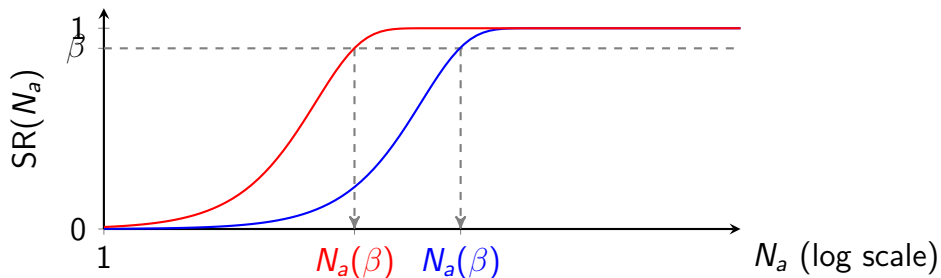
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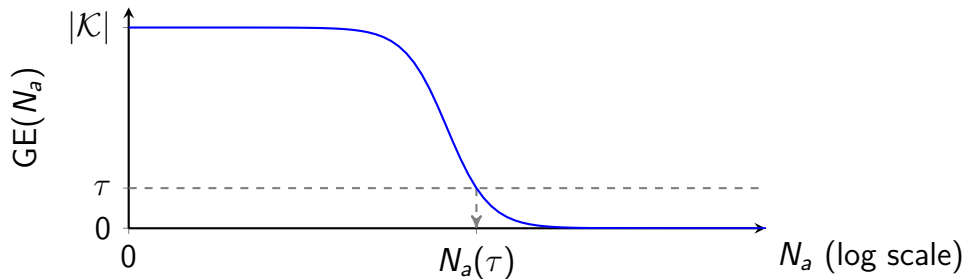
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- **Q4 [full key to D&C]:** Prove that  $N_a(\beta, \text{full}) = N_a(\beta^{\frac{n}{|\mathcal{K}|}}, \text{word})$

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# Assessing an Attack: the Guessing Entropy

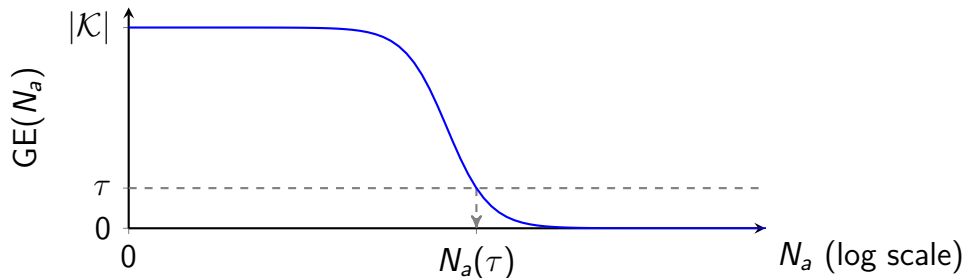


- GE: average rank of the right key, among all key hypotheses

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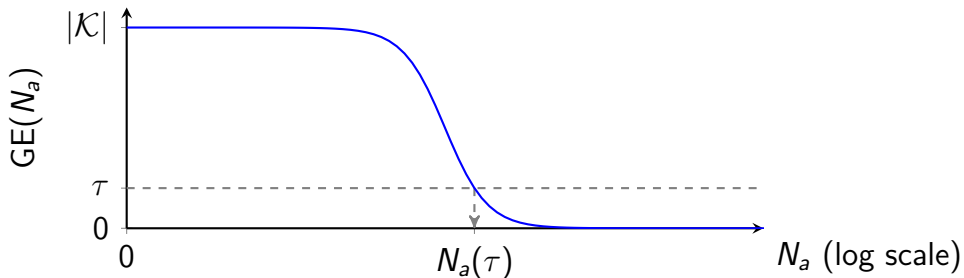


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- Allows to quantify the key remaining *enumeration work*

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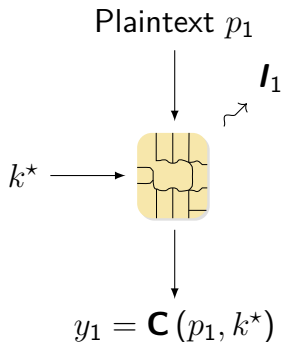


- GE: average rank of the right key, among all key hypotheses
- Allows to quantify the key remaining *enumeration work*
- **Q5 [full key to D&C]:**<sup>9</sup> Prove that  $N_a(\tau, \text{full}) \geq N_a(\tau^{\frac{n}{|\mathcal{K}|}}, \text{word})$

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# Optimal Attack: Maximum Likelihood<sup>10</sup>

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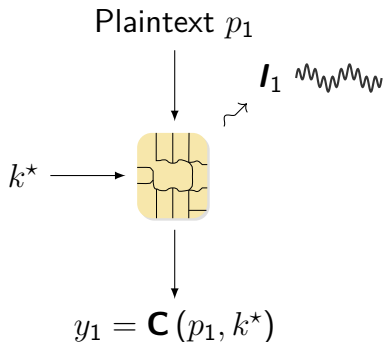


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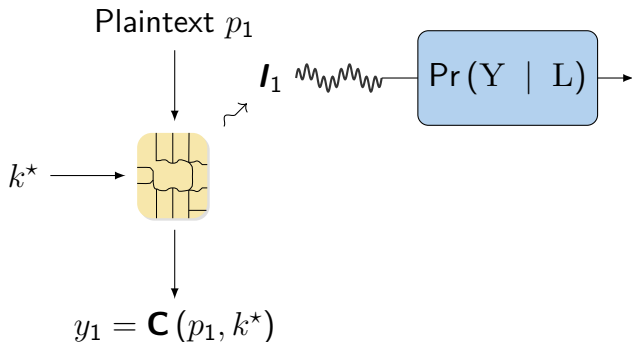
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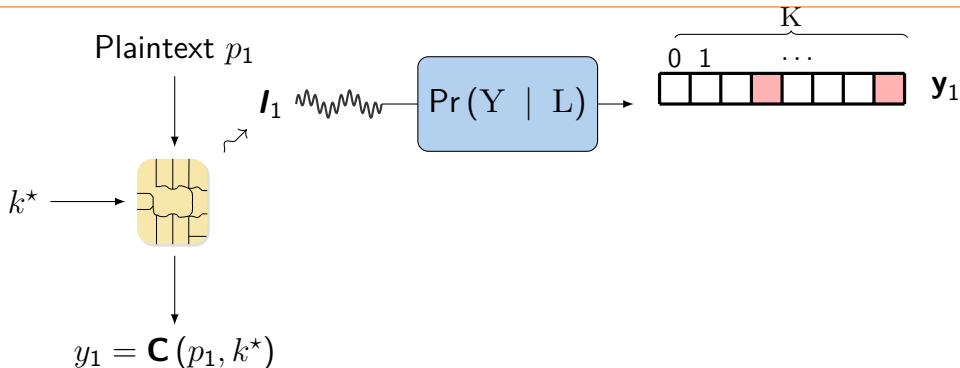
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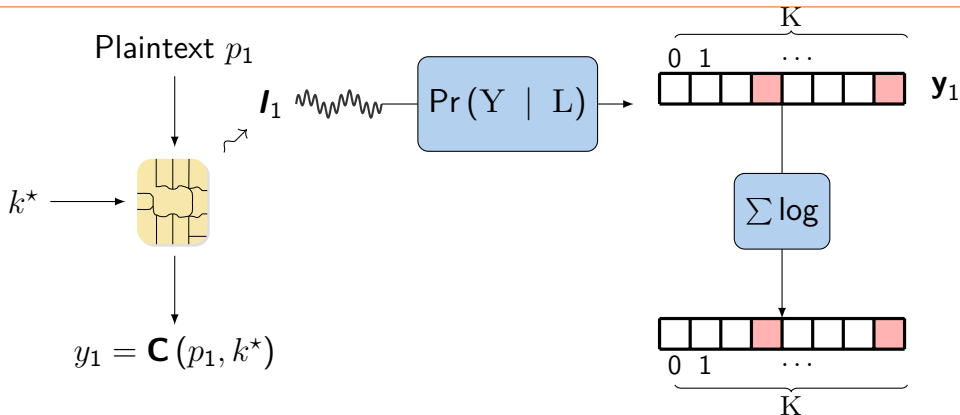
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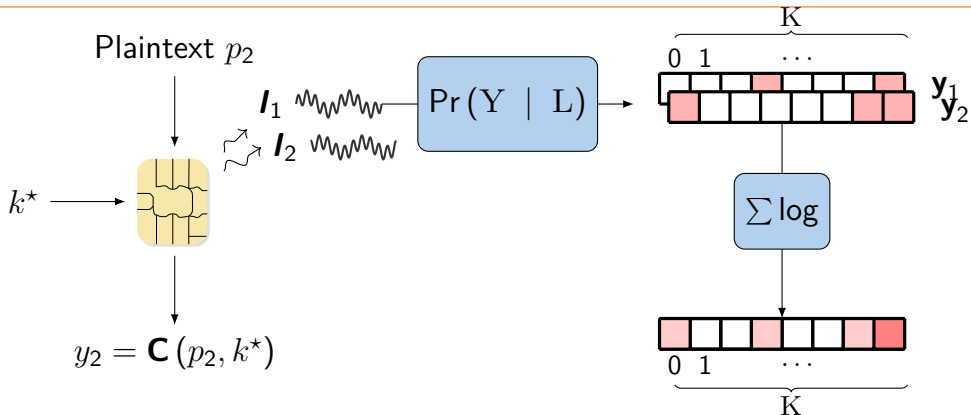
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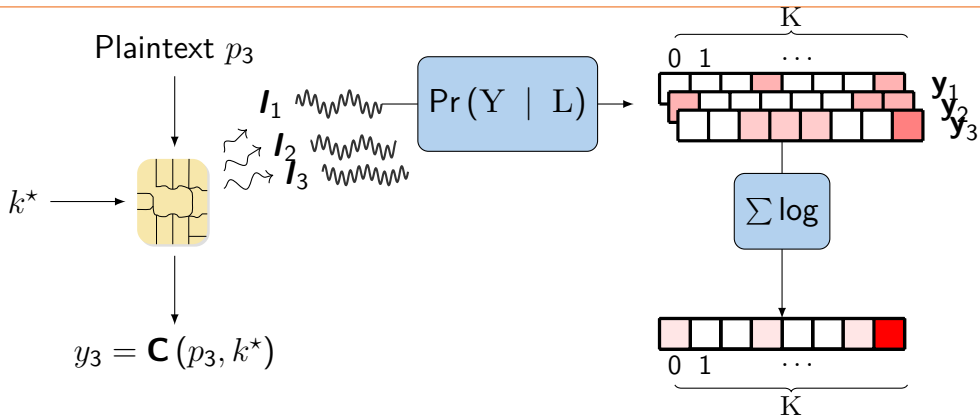
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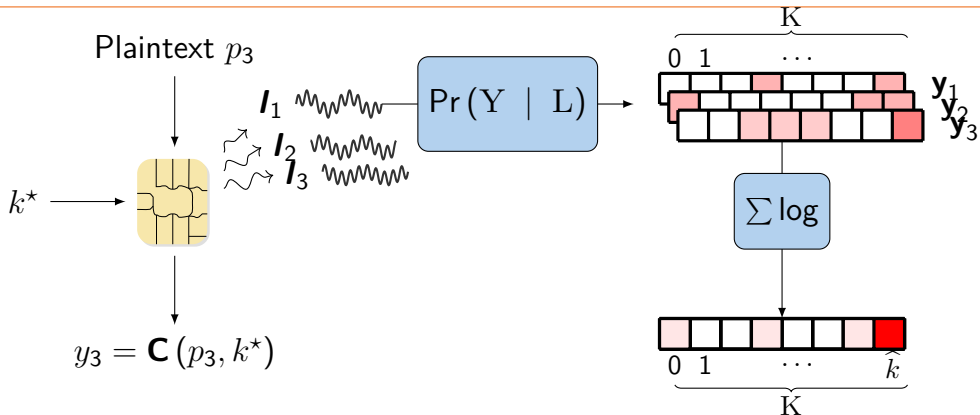


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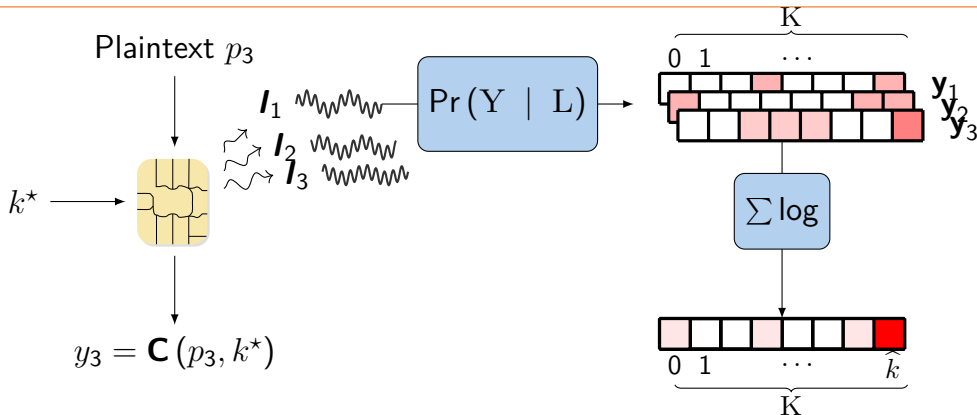
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# Optimal Attack: Maximum Likelihood<sup>10</sup>



**Problem:  $\Pr(Y | L)$  unknown (device-dependent)**

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# Content

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---

### The Core Problem: Make & Certify a Device as Secure

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## Masking

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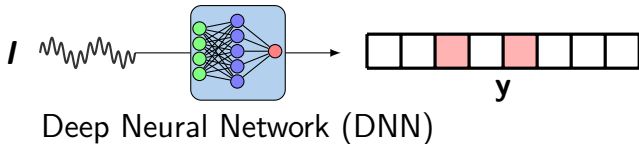
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# Deep Learning (DL) for SCA



General way to modelize, *i.e.*, to convert leakage into probabilities

$$F : \left\{ \begin{array}{l} \mathcal{L} \longrightarrow \mathcal{P}(\mathcal{Y}) \\ I \longmapsto \mathbf{y} = F(I) \approx \Pr(Y \mid \mathbf{L} = I) \end{array} \right. \quad (1)$$

$F(I)$ : output of a Directed Acyclic Graph (DAG) of computation:

Each node: elementary function  $f_i(\cdot, \theta_i)$

$\theta_i$ : *parameters* fully describing  $f_i$

Shape of the DAG, nature of the classes of functions: *architecture* of the DNN.

# Profiled SCA = Supervised Learning Problem

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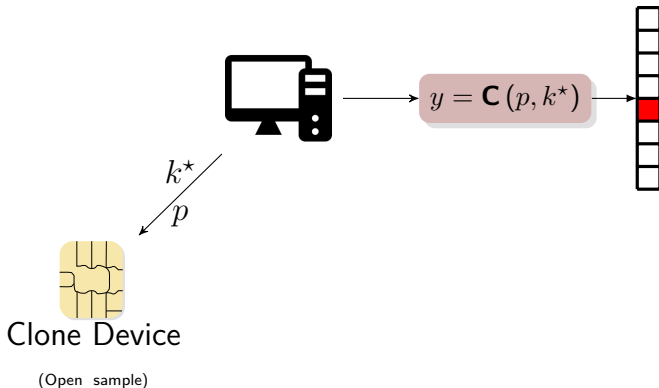


Clone Device

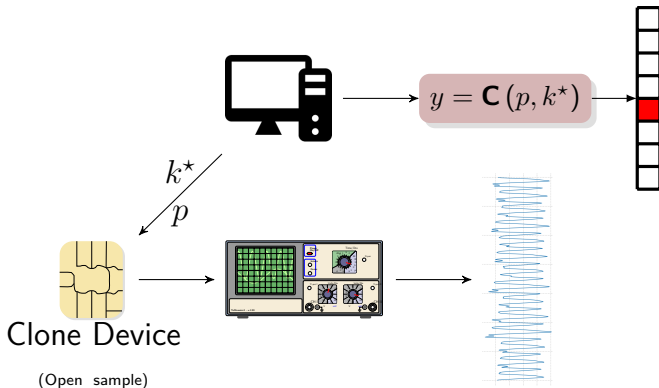
(Open sample)

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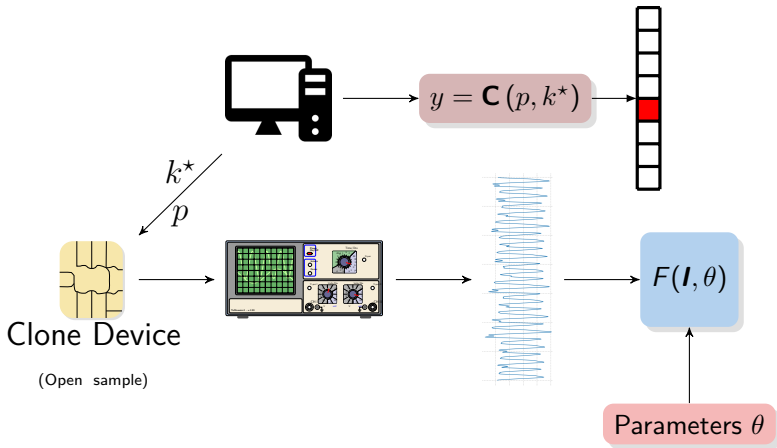


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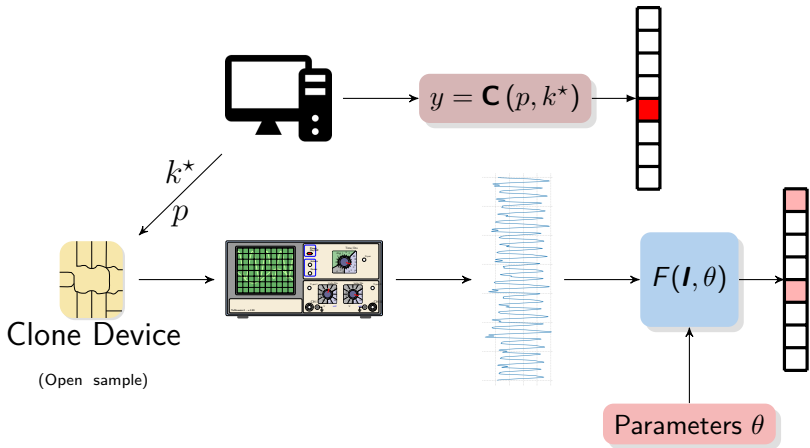




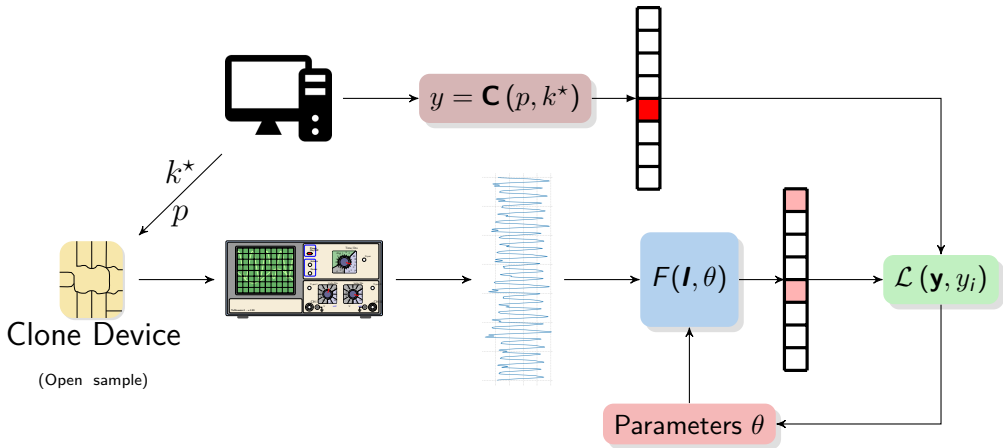
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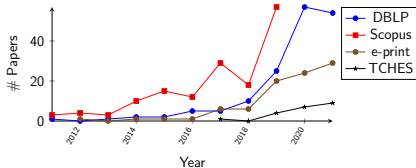
# Profiled SCA = Supervised Learning Problem



$\mathcal{L}()$ : loss function to minimize, with *gradient descent*

# The Deep Learning (DL) hype in SCA

- **Space 2016:** DL breaks *masking*<sup>11</sup>
- **Ches 2017:** CNNs efficiently tackles *misalignment*<sup>12</sup>
- **Ches 2019:** non-profiled attacks<sup>13</sup>
- *De facto* standard for evaluations
- Dedicated sessions in conferences



<sup>11</sup>Maghrebi, Portigliatti, and Prouff, “Breaking Cryptographic Implementations Using Deep Learning Techniques”

<sup>12</sup>Cagli, Dumas, and Prouff, “Convolutional Neural Networks with Data Augmentation Against Jitter-Based Countermeasures - Profiling Attacks Without Pre-processing”

<sup>13</sup>Timon, “Non-Profiled Deep Learning-based Side-Channel attacks with Sensitivity Analysis”

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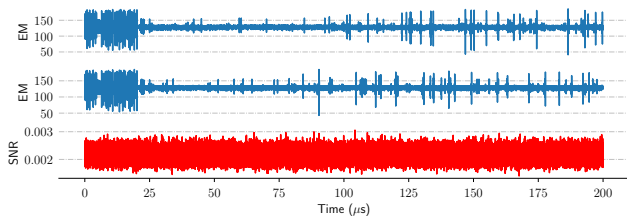
## What about Post-Quantum?

# Use Case: SCA against code polymorphism I

Implementation of AES from mbedTLS on ARM-Cortex M4 architecture

T-table implementation with 32 bit variables

100,000 traces acquired for each target ( $\leq$  a day)



**Figure:** Two examples of traces (blue) and the Signal-to-Noise Ratio (SNR) (red)

No SNR peak  $\implies$  a layman attacker fails, even with  $N_a = 10^5$  traces

# Use Case: SCA against code polymorphism II<sup>14</sup>

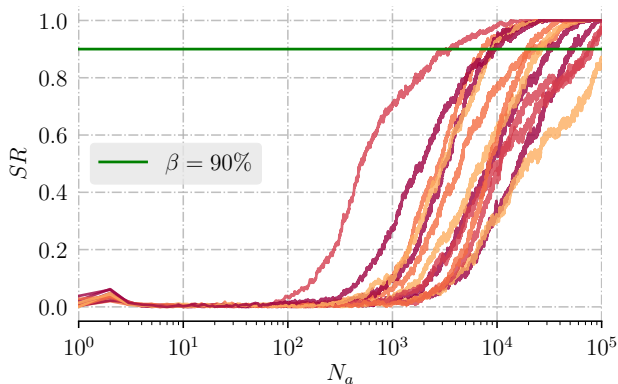


Figure: Layman Attacker with Re-alignment

<sup>14</sup>Masure et al., “Deep Learning Side-Channel Analysis on Large-Scale Traces - A Case Study on a Polymorphic AES”.

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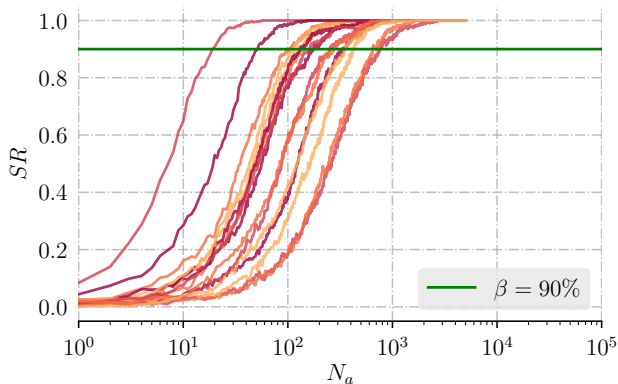


Figure: Attacker with Re-alignment and a Clone device

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## Use Case: SCA against code polymorphism II<sup>14</sup>

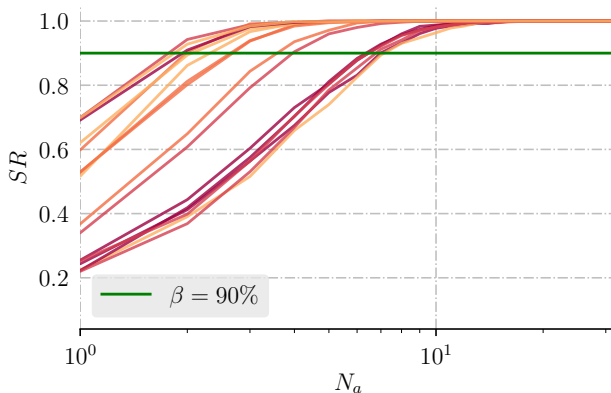


Figure: Attacker without Re-alignment but with a Clone device and deep learning

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# Post-Mortem Sensitivity Analysis

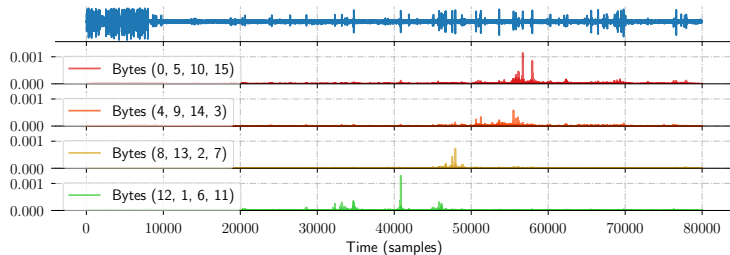


Figure: Gradient Visualization against code polymorphism

Forensics: *“Where does my leakage come from?”*

0	4	8	12
1	5	9	13
2	6	10	14
3	7	11	15

X0X1X2X3

(a) AES state after the first AddRoundKey

0	4	8	12
5	9	13	1
10	14	2	6
15	3	7	11

X0X1X2X3

(b) AES state at the end of the ShiftRows

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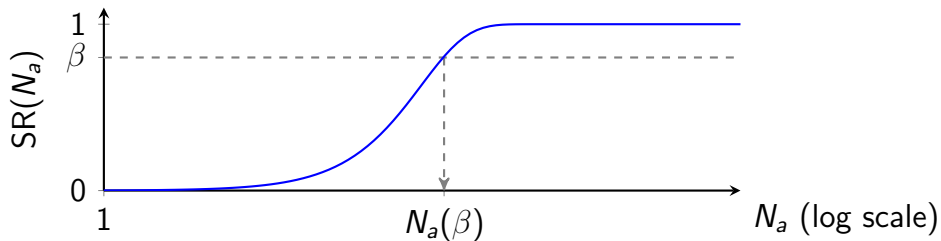
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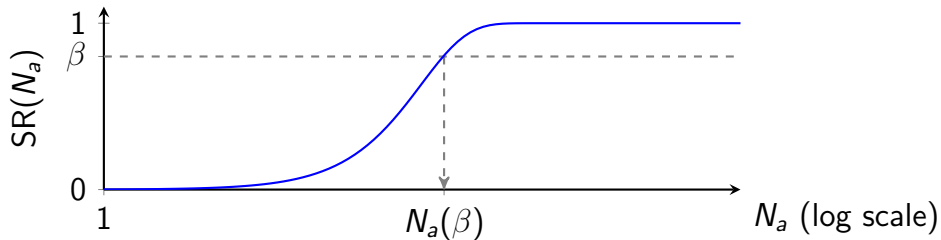
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Needs to estimate the whole Success Rate (SR) curve to derive  $N_a(\beta)$



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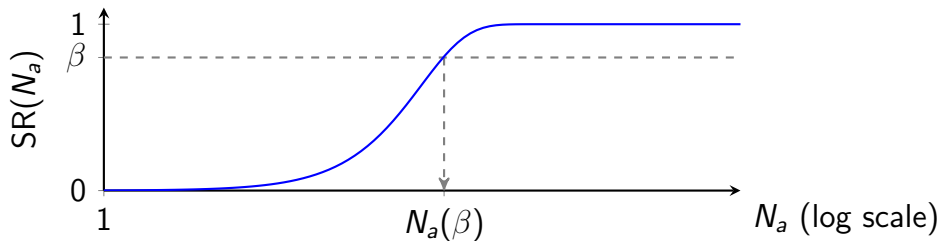
Naive est. of  $N_a(\beta)$  expensive:

Requires  $N_v \geq N_a(\beta)$  ✓

Complexity  $\mathcal{O}(N_{est} \cdot N_v)$  ✗

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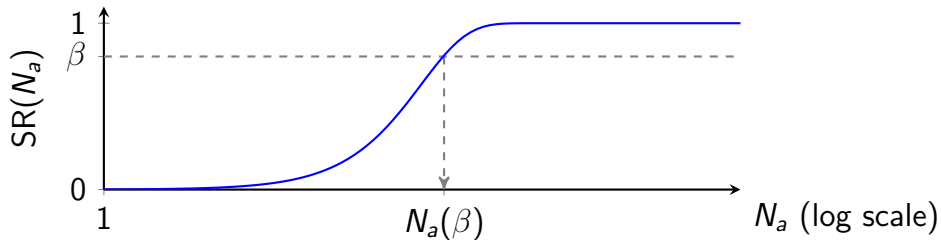
Cheaper alternative: re-sampling

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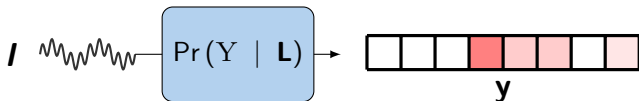
Biased method ✓

**Wish: find a shortcut to estimate  $N_a(\beta)$**

# Shortcut

---

Solution: characterize the predictions of the adversary's model



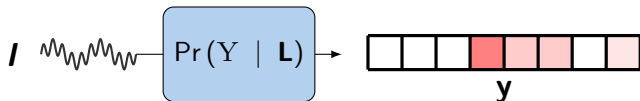
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<sup>15</sup>D: Kullback - Leibler (KL) divergence, total variation, Euclidean norm, ...



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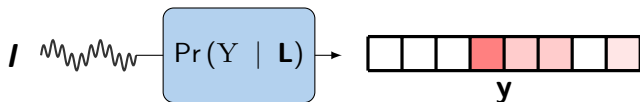
If, the adversary gets:



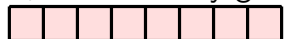
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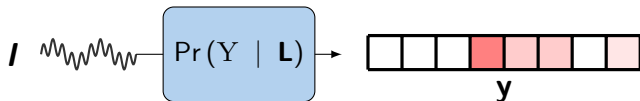
Very noisy leakage

$k$  unpredictable:  $N_a(\beta) \rightarrow \infty$ , if  $\beta > \frac{1}{|\mathcal{K}|}$

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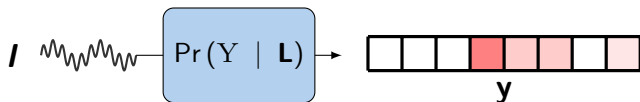
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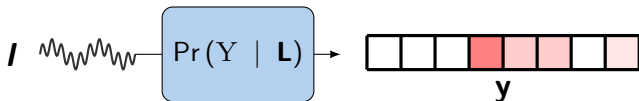
Low-noise leakage

Exact prediction:  $N_a(\beta) = 1$

<sup>15</sup>D: KL divergence, total variation, Euclidean norm, ...

# Shortcut

Solution: characterize the predictions of the adversary's model



## $\delta$ -NOISY ADVERSARY

All the p.m.f.s accessed by the adversary are  $\delta$ -close<sup>15</sup> to the uniform:

$$D \left( \begin{array}{|c|c|c|c|c|c|c|c|} \hline \square & \square & \square & \text{red} & \text{pink} & \text{pink} & \square & \text{pink} \\ \hline \end{array}, \begin{array}{|c|c|c|c|c|c|c|c|} \hline \text{pink} & \text{pink} & \text{pink} & \text{pink} & \text{pink} & \text{pink} & \text{pink} & \text{pink} \\ \hline \end{array} \right) \leq \delta$$

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# A First Attempt: the Statistical Distance (SD)

---

## DEFINITION (STATISTICAL DISTANCE (SD))

Statistical Distance (SD) upper bounds the probability to distinguish two leakage distributions given two different keys (useful for cryptographers):

$$\text{SD}(Y; \mathbf{L}) = \mathbb{E}_{\mathbf{L}} [\text{TV}(p_{Y|\mathbf{L}}; p_Y)] , \text{ where } \text{TV}(p; m) = \frac{1}{2} \sum_{y \in \mathcal{Y}} |p(y) - m(y)|$$

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<sup>16</sup>Lower bound: tensorization of SD. Upper bound: Chernoff inequality (+ Slud's for tightness).

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Denote  $\text{SD}(Y; \mathbf{L})$  by  $\delta$ , then the following bounds are tight (**Q6**: prove it):<sup>16</sup>

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---

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# A Second Attempt: the Mutual Information (MI)

---

## DEFINITION (MUTUAL INFORMATION (MI))

$$\text{MI}(Y; \mathbf{L}) = \mathbb{E}_{\mathbf{L}} \left[ D(p_{Y|\mathbf{L}} \parallel p_Y) \right], \text{ where } D(p \parallel m) = \sum_{y \in \mathcal{Y}} p(y) \log \left( \frac{p(y)}{m(y)} \right)$$

## LEMMA (FANO INEQUALITY)

Denote  $\text{MI}(Y; \mathbf{L})$  by  $\delta$ , then the following inequality is tight (Shannon's coding theorem):<sup>17</sup>

$$\frac{f(\beta)}{\delta} \leq N_a(\beta)$$

---

<sup>17</sup>Cover and Thomas, *Elements of information theory* (2. ed.)

# Use Cases with Univariate Leakage, Gaussian Noise

---

Leakage model of shape  $L = \delta(\mathcal{Y}) + N$

→ Upper bound of MI from Signal-to-Noise Ratio (SNR)

$$N_a(\beta) \geq \frac{f(\beta)}{\text{MI}(\mathcal{Y}; L)}$$

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→ Generalizes the data complexity for correlation attack:

$$N_a^{corr} \approx \frac{28}{\rho^2}$$

# Reduction to MI estimation

---

Estimating a Mutual Information is generally hard:

---

<sup>18</sup>Paninski, “Estimation of Entropy and Mutual Information”.

<sup>19</sup>Masure et al., “Information Bounds and Convergence Rates for Side-Channel Security Evaluators”.

# Reduction to MI estimation

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✗ No unbiased estimator<sup>18</sup>

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✗ No unbiased estimator<sup>18</sup>

✗ *Empirical* estimator:

→ Positively biased in average ✓

→ Suffers from *curse of dimensionality*<sup>19</sup> ✗

---

<sup>18</sup>Paninski, “Estimation of Entropy and Mutual Information”.

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# Reduction to MI estimation

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## DEFINITION (PI)

$$\text{MI}(Y; \mathbf{L}) = \mathbb{E}_{\mathbf{L}} \left[ D(p_{Y|\mathbf{L}} \parallel p_Y) \right]$$

---

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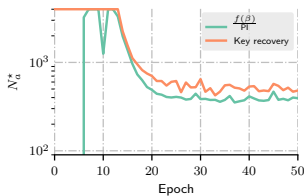
$$\text{PI}(Y; \mathbf{L}; \theta) = \mathbb{E}_{\mathbf{L}} [D(F(\mathbf{L}, \theta) \parallel p_Y)] \leq \text{MI}(Y; \mathbf{L})$$

---

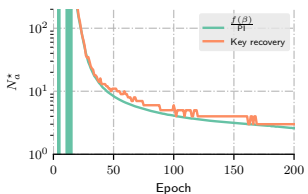
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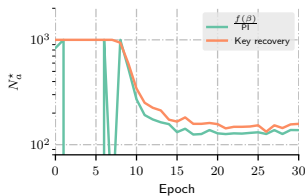
# Examples



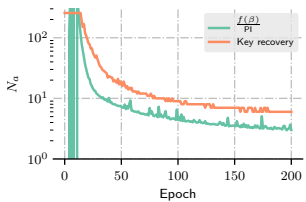
(a) AES FPGA



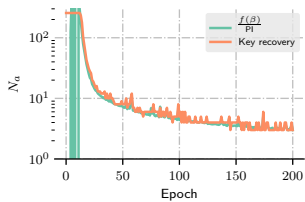
(b) AES with misalignment



(c) AES soft with masking



(d) Polymorphic AES



(e) Polymorphic AES

# PI and other Surrogates

---

PI can be well<sup>20</sup> estimated/optimized over the open sample:

- Estimation error  $\epsilon = \mathcal{O}\left(\frac{\text{poly}(\mathcal{H})}{N_p}\right)$ , where
  - $N_p = \# \text{profiling traces}$
  - $\mathcal{H}$ : class of models (neural network, #parameters, ...)

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  - $N_p = \# \text{profiling traces}$
  - $\mathcal{H}$ : class of models (neural network, #parameters, ...)
- We want the estimation error  $\epsilon \lesssim \text{MI}(Y; L) \implies$

$$N_p(\epsilon) \geq \Omega\left(\frac{\text{poly}(\mathcal{H})}{\text{MI}(Y; L)}\right) \gtrsim N_a(\beta)$$

→ “Profiling is as costly as attacking”

---

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# Wrap-Up

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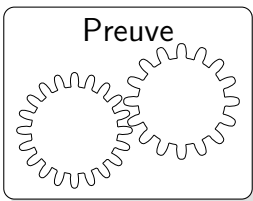
Side-Channel Analysis is a threat as powerful (but cheaper) as quantum computers

Need to assess the security level against SCA in an affordable manner  
 $\implies$  evaluation shortcuts

Tomorrow: presentation of masking, how to implement it, security analysis

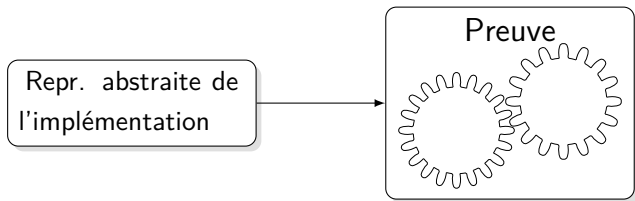
# Preuves de Sécurité pour Masquage

---



# Preuves de Sécurité pour Masquage

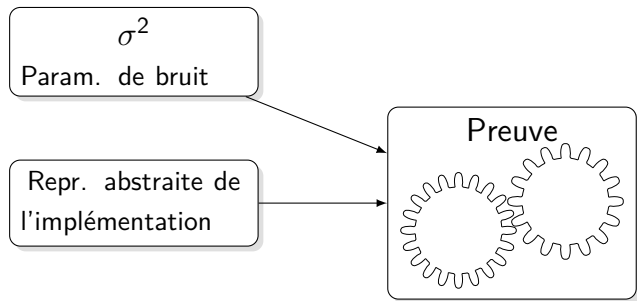
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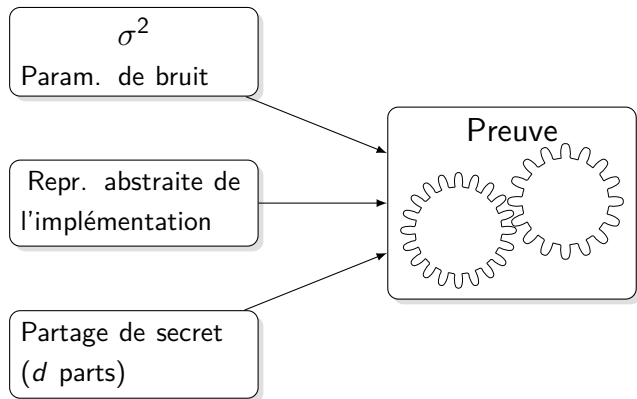
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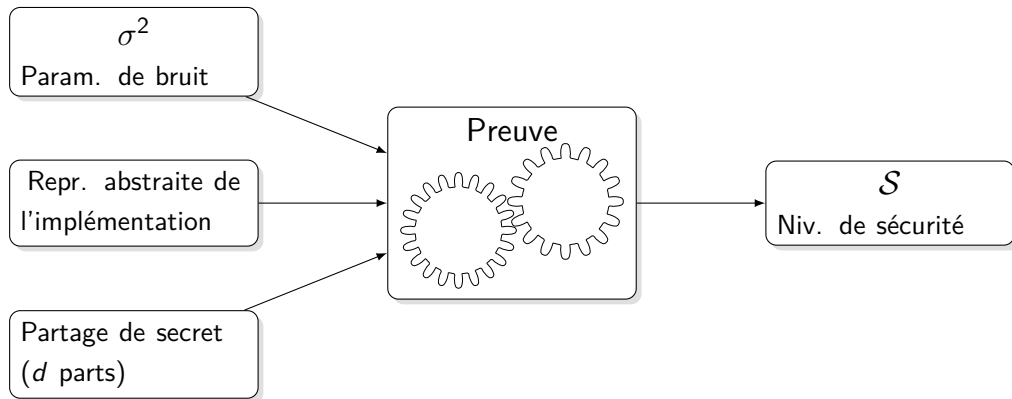


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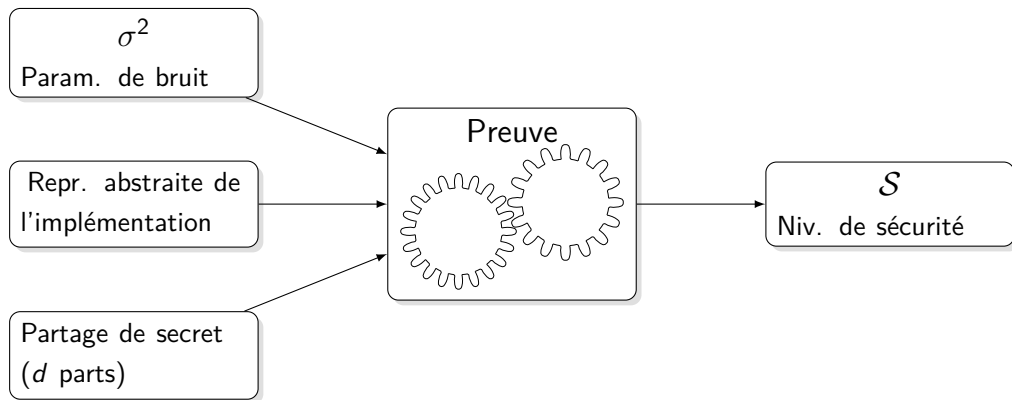
---



# Preuves de Sécurité pour Masquage



# Preuves de Sécurité pour Masquage



“Toute attaque nécessite  $\mathcal{S}$  observations ”

# Content

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---

The Core Problem: Make & Certify a Device as Secure

Security Certification

Deep Learning Attacks

Use Case: Polymorphic Implementation

More Evaluation Shortcuts

**Masking**

Security Analysis for a Single Encoding

Computing on Masked Secrets

Security Analysis over Computations

What about Post-Quantum?

# Masking: what is that ?

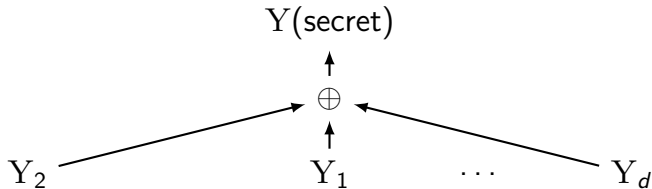
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Masking, a.k.a. *MPC on silicon*:<sup>2122</sup> secret sharing over a finite field  $(\mathbb{F}, \oplus, \otimes)$   
 $Y(\text{secret})$

# Masking: what is that ?

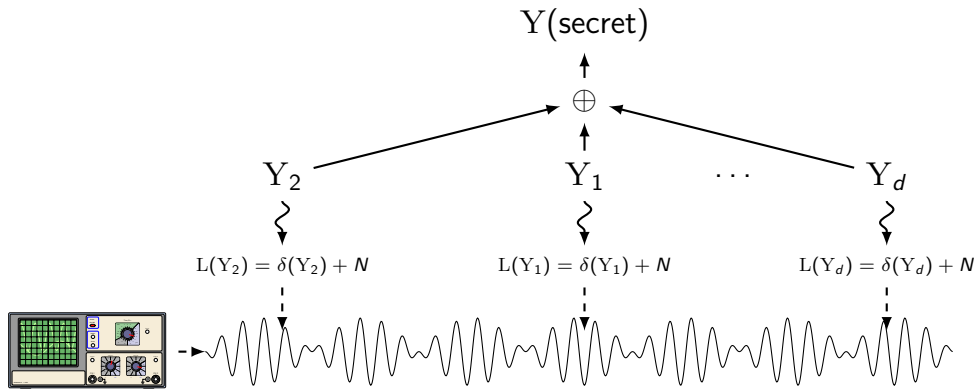
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Masking, a.k.a. *MPC on silicon*:<sup>2122</sup> secret sharing over a finite field  $(\mathbb{F}, \oplus, \otimes)$



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Masking, a.k.a. *MPC on silicon*:<sup>2122</sup> secret sharing over a finite field  $(\mathbb{F}, \oplus, \otimes)$



<sup>21</sup>Chari et al., "Towards Sound Approaches to Counteract Power-Analysis Attacks".

<sup>22</sup>Goubin and Patarin, "DES and Differential Power Analysis (The "Duplication" Method)".



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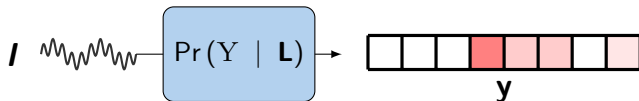
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### Perspectives

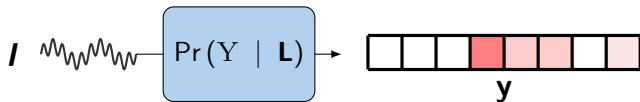
# The Noisy Leakage Model

In this model, for each intermediate computation, the adversary gets a probability distribution about its operands:

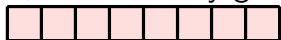


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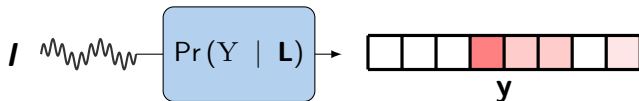


If, the adversary gets:

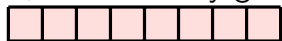


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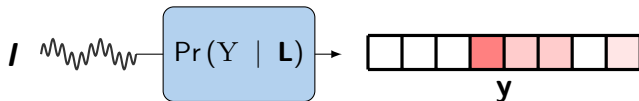


Very noisy

Sensitive computation unpredictable

# The Noisy Leakage Model

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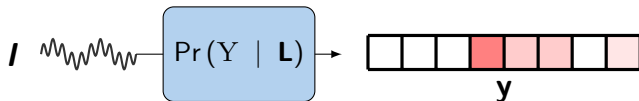


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In this model, for each intermediate computation, the adversary gets a probability distribution about its operands:



If, the adversary gets:



Low-noise

Exact prediction of the sensitive computation

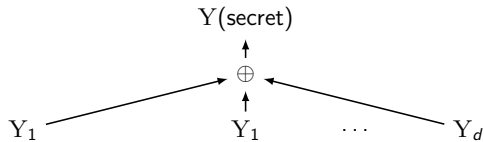
# The Effect of Masking

---

Y(secret)

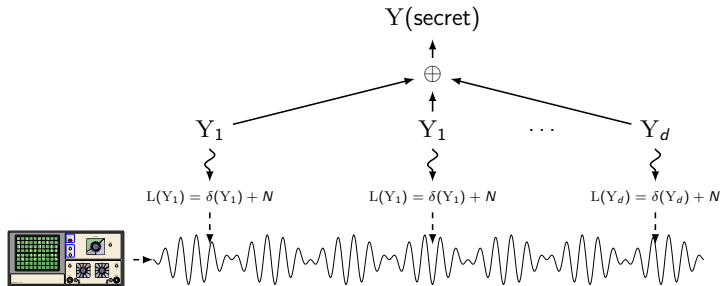
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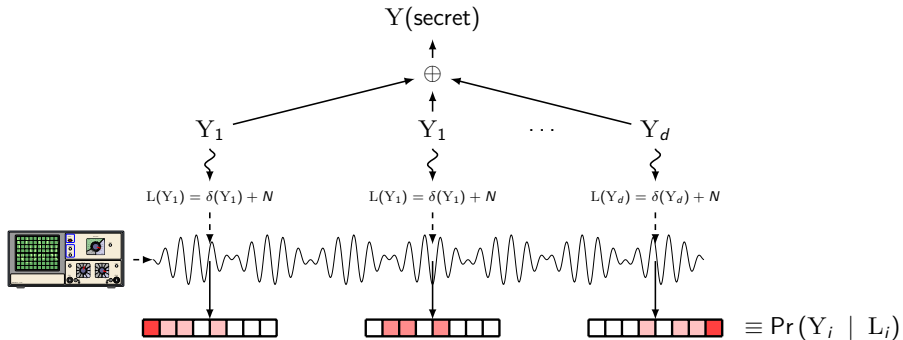




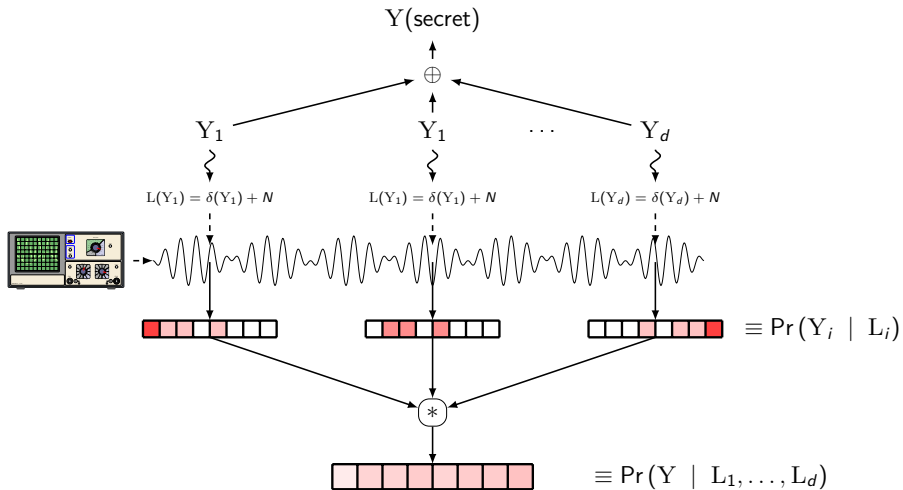
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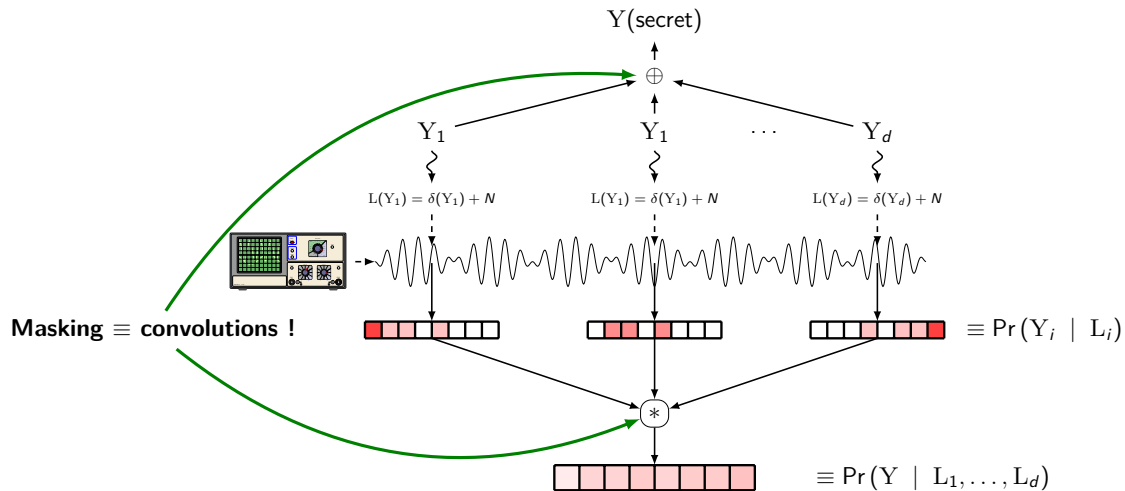
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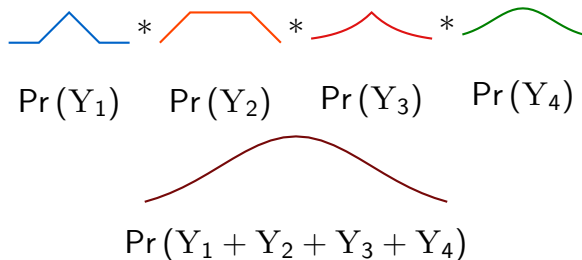


# The Effect of Masking



# The Secret Power of Convolutions

Central Limit Theorem: Assume real-valued random variables  $Y_i$



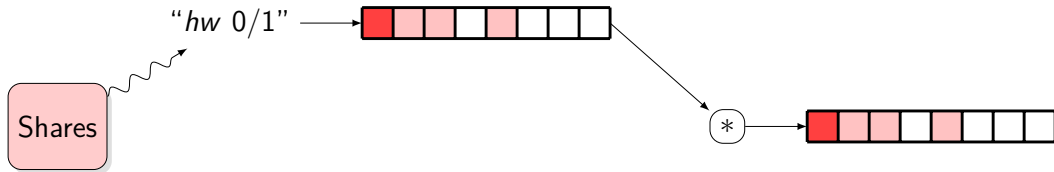
Then the sum is (approximately) distributed like a Gaussian<sup>23</sup>

Interesting property of Gaussian: maximizes the entropy (*i.e.*, uncertainty)<sup>24</sup>

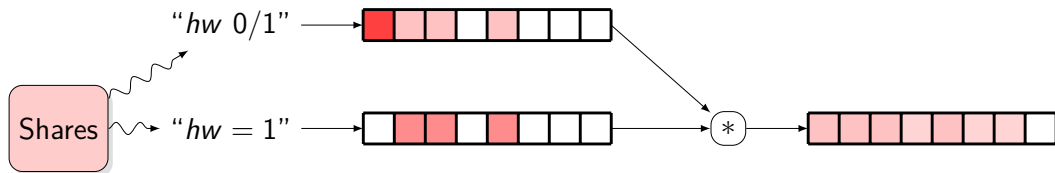
<sup>23</sup>With mild assumptions, but we'll get back to that ...

<sup>24</sup>Out of all Probability Density Functions (p.d.f.s) of same mean and variance

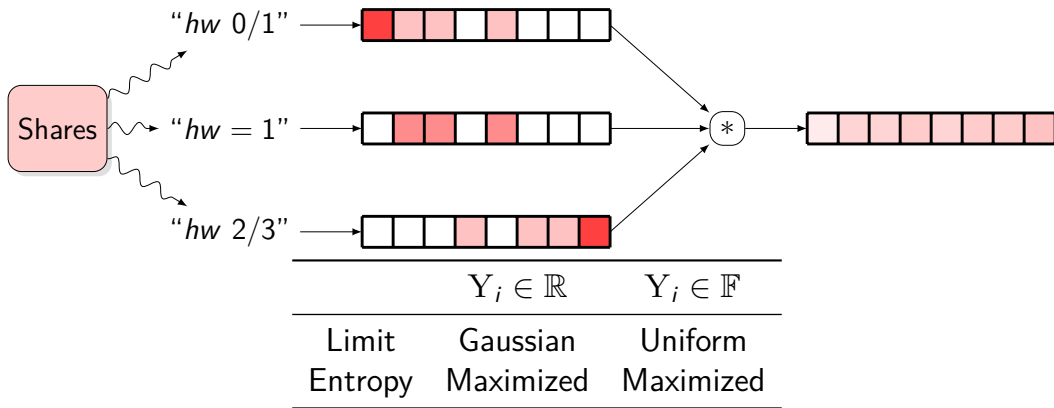
# CLT also Works in Finite Groups/Fields !



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# CLT also Works in Finite Groups/Fields !



Fast Fourier Transform also apply over finite fields !



# Quantitative version of CLT

---

## *THEOREM (MRS. GERBER'S LEMMA<sup>25</sup>)*

*Given  $Y = Y_1 \oplus \dots \oplus Y_d$ , and each  $Y_i$  with (indep.) side information  $L_1, \dots, L_d$ , then for  $\eta^{-1} = 2 \log(2)$ :*

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<sup>25</sup>Béguinot et al., “Removing the Field Size Loss from Duc et al.’s Conjectured Bound for Masked Encodings”.

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$$\text{MI}(Y; \mathbf{L}) \leq \prod_{i=1}^d \frac{\text{MI}(Y_i; L_i)}{\eta} + \mathcal{O} \left( \prod_{i=1}^d \text{MI}(Y_i; L_i)^2 \right) \text{ in } \mathbb{F}_{2^n}$$

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→ Security  $\propto \frac{1}{\text{MI}(Y; \mathbf{L})} \implies$  increases **exponentially fast** with  $d$  ✓

→ Independent of the adversary ✓

---

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# Convolution = Noise Amplification

**Simulation, for  $\mathbb{F}_{2^n}$ :**  $L(Y_i) = hw(Y_i) + \mathcal{N}(0; \sigma^2)$ ,  $hw$  = Hamming weight

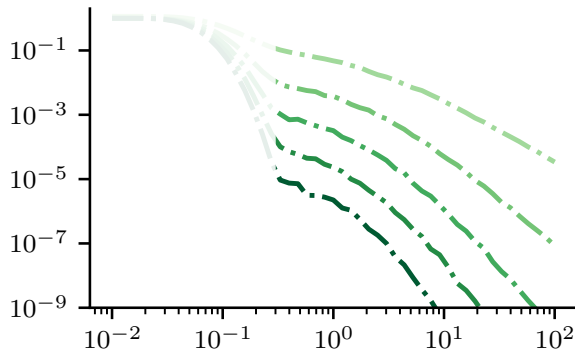
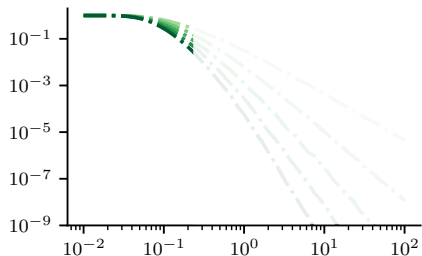


Figure:  $MI(Y; \mathbf{L})$  vs.  $\sigma^2$ ,  $2 \leq d \leq 6$

# Masking in a Low-Noise Setting

Does masking always work in a low-noise setting ?



## Observation:

Secret always leaks  $> 1$  bit, regardless of  $d$

## Explanation:

$$\text{lsb}(Y_1 \oplus \dots \oplus Y_d) = \text{lsb}(Y_1) \oplus \dots \oplus \text{lsb}(Y_d)$$

Figure:  $MI(Y; \text{Trace})$  vs.  $\sigma^2$ ,  $2 \leq d \leq 6$

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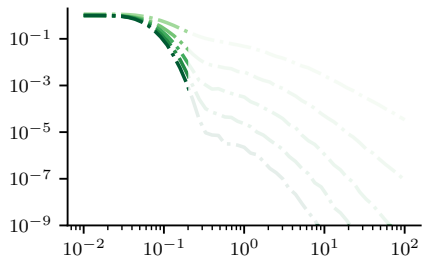


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$$\text{hw}(Y_1 \oplus \dots \oplus Y_d) = \sum_i \text{hw}(Y_i) - 2 \cdot (\dots)$$

Parity of  $\text{hw}(Y)$ : **cosets of  $\mathbb{F}_{2^n}$**

**Corollary:** parallelism is no cure either

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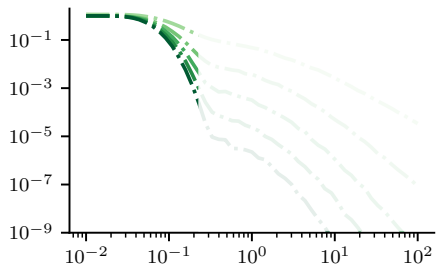
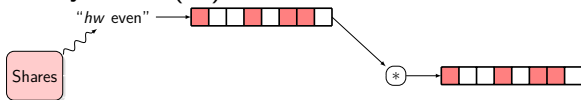


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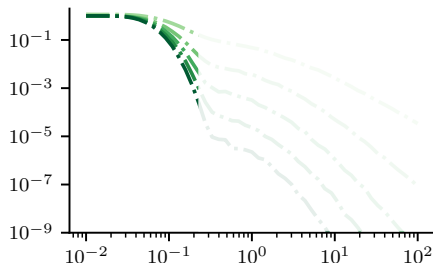
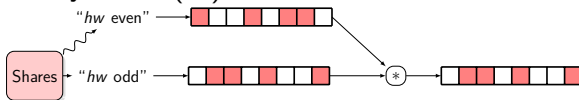


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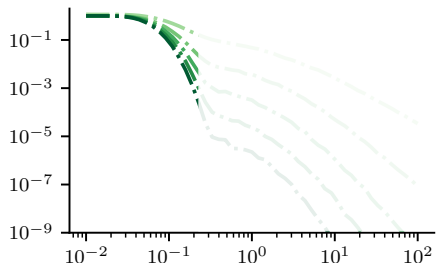
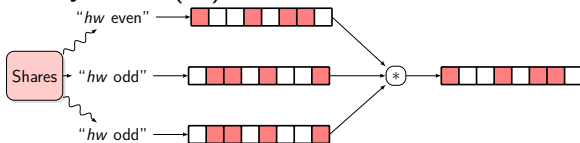


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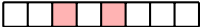
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# Conditions for Sound Masking

---

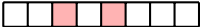
What conditions the distributions  of each share must fit?

---

<sup>26</sup>Stromberg, “Probabilities on a Compact Group”.

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“CENTRAL LIMIT THEOREM” (QUALITATIVE)<sup>26</sup>

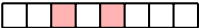
Conv. to uniform  $\iff$  support *not* contained in any non-trivial coset of  $\mathbb{F}$

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“CENTRAL LIMIT THEOREM” (QUALITATIVE)<sup>26</sup>

Conv. to uniform  $\iff$  support *not* contained in any non-trivial coset of  $\mathbb{F}$

In  $\mathbb{R}$  : mild assumption

→ Only  $\mathbb{Z}$  and  $\mathbb{Q}$  (and their respective subgroups)

→ Negligible measure over  $\mathbb{R}$

In finite  $\mathbb{F}$ : no longer mild in finite fields ...

---

<sup>26</sup>Stromberg, “Probabilities on a Compact Group”.

# Two Solutions

---

# Two Solutions

---

**Solution 1:** Make sure to leak  $< 1$  bit per share:

- Support of PMF always larger than any coset
- Work with any  $\mathbb{F}$  (usually chosen to fit the cipher) ✓
- **Leakage-dependent: not always verified** ✗

# Two Solutions

---

**Solution 2:** Choose  $\mathbb{F}$  without any non-trivial subgroup, *i.e.*,  $\mathbb{F}_p$ ,  $p$  prime:

- No assumption on the leakage ✓
- Major change of paradigm:
  - Fix  $\mathbb{F}$  masking-friendly first,
  - Then build crypto upon it ✓



# Comparing Binary and Prime Fields: a Simulation

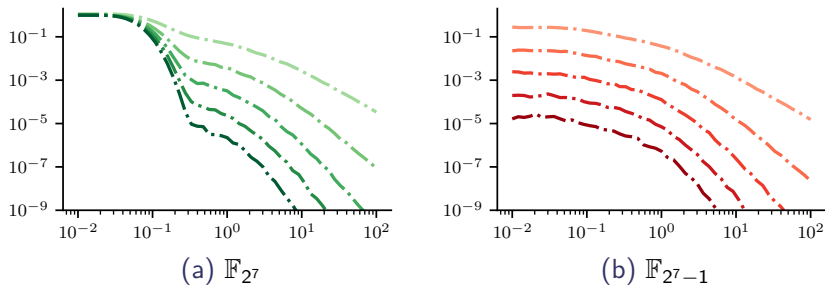


Figure: Comparing binary and prime fields.

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# Threshold Probing Model

---

## DEFINITION ( $t$ -PRIVACY)

Any tuple of  $t$  intermediate values  $\perp$  secrets

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<sup>27</sup>Not  $\iff$ , see Bordes, “Security of symmetric primitives and their implementations”, Example 5.5

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A set of probes  $\mathcal{P}$  in a circuit  $\mathbb{C}$  can be simulated with the input shares  $\mathcal{I}$  if there exists an algorithm  $\mathcal{S}$  (the *simulator*) such that

$$\mathcal{P} \stackrel{d}{=} \mathcal{S}(\mathcal{I})$$

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## DEFINITION ( $t$ -NON-INTERFERENCE (NI))

$\mathbb{C}$  is  $t$ -NI if *any* set of  $t$  probes is simulatable by *at most*  $t$  shares of each input

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<sup>27</sup>Not  $\iff$ , see Bordes, “Security of symmetric primitives and their implementations”, Example 5.5

# Threshold Probing Model

---

## DEFINITION ( $t$ -PRIVACY)

Any tuple of  $t$  intermediate values  $\perp$  secrets

## DEFINITION (SIMULATABILITY)

A set of probes  $\mathcal{P}$  in a circuit  $\mathbb{C}$  can be simulated with the input shares  $\mathcal{I}$  if there exists an algorithm  $\mathcal{S}$  (the *simulator*) such that

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$$\mathcal{P} \stackrel{d}{=} \mathcal{S}(\mathcal{I}) \iff \mathcal{P} \perp \text{all inputs except } \mathcal{I}$$

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# The Composition Paradigm

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Idea to make a circuit NI:

---

<sup>28</sup>Ishai, Sahai, and Wagner, “Private Circuits: Securing Hardware against Probing Attacks”.

<sup>29</sup>Rivain and Prouff, “Provably Secure Higher-Order Masking of AES”.

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- Et voilà !

**Issue:** NI is not composable<sup>30</sup> **✗Q8:** example on white board

---

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# Strong Non-Interference<sup>32</sup>

---

## DEFINITION ( $t$ -STRONG NON-INTERFERENCE)

A gadget is  $t$ -SNI if any set of  $t_1$  internal probes and  $t_2$  output probes can be simulated with  $t_1$  shares of each input sharing, and

$$t = t_1 + t_2$$

$\rightarrow \text{SNI} \implies \text{NI} \implies \text{privacy}$

Other composable notions: SNIo, PINI<sup>31</sup>, robust probing, glitch-extended, ...

---

<sup>31</sup>Cassiers and Standaert, “Trivially and Efficiently Composing Masked Gadgets With Probe Isolating Non-Interference”.

<sup>32</sup>Barthe et al., “Strong Non-Interference and Type-Directed Higher-Order Masking”.

# Masked addition gadget

---

Inputs:

$$\llbracket A \rrbracket = (A_1, \dots, A_d)$$

$$\llbracket B \rrbracket = (B_1, \dots, B_d)$$

Output:

$$\llbracket C \rrbracket = (C_1, \dots, C_d)$$

such that

$$\sum_i C_i = \left( \sum_i A_i \right) \oplus \left( \sum_i B_i \right)$$



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$$C_1 = A_1 \oplus B_1$$

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- NI, but not SNI ✗
- $t$ -NI +  $t$ -SNI refresh  $\implies$   $t$ -SNI ✓

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- NI, but not SNI ✗
- $t$ -NI +  $t$ -SNI refresh  $\implies$   $t$ -SNI ✓
- Generalization: share-wise application of any affine map

# Masked multiplication gadget

---

Inputs:

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$$\llbracket B \rrbracket = (B_1, \dots, B_d)$$

Output:

$$\llbracket C \rrbracket = (C_1, \dots, C_d)$$

such that

$$\sum_i C_i = \left( \sum_i A_i \right) \otimes \left( \sum_i B_i \right)$$

# Masked multiplication gadget

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BadMult algorithm:

$$C_1 = (A_1 \otimes B_1) \oplus (A_1 \otimes B_2) \oplus (A_1 \otimes B_3)$$

$$C_2 = (A_2 \otimes B_1) \oplus (A_2 \otimes B_2) \oplus (A_2 \otimes B_3)$$

$$C_3 = (A_3 \otimes B_1) \oplus (A_3 \otimes B_2) \oplus (A_3 \otimes B_3)$$

Correct, but not 2-NI. **Q7:** Why ?

# Masked multiplication gadget

Inputs:

$$\llbracket A \rrbracket = (A_1, \dots, A_d)$$

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Correct, but not 2-NI. **Q7:** Why ?

# Masked multiplication gadget

Inputs:

$$\llbracket A \rrbracket = (A_1, \dots, A_d)$$

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$$C_1 = (A_1 \otimes B_1) \oplus (A_1 \otimes B_2 \oplus R_1) \oplus (A_1 \otimes B_3 \oplus R_2)$$

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$$C_3 = (A_3 \otimes B_1 \oplus R_2) \oplus (A_3 \otimes B_2 \oplus R_3) \oplus (A_3 \otimes B_3)$$

• SecMult is  $(d - 1)$ -SNI ✓



# Masked multiplication gadget

Inputs:

$$\llbracket A \rrbracket = (A_1, \dots, A_d)$$

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$$C_3 = (A_3 \otimes B_1 \oplus R_2) \oplus (A_3 \otimes B_2 \oplus R_3) \oplus (A_3 \otimes B_3)$$

- SecMult is  $(d - 1)$ -SNI ✓
- If  $\llbracket B \rrbracket = (1, 0, \dots, 0)$ , then  
 $\text{SecMult}(\llbracket A \rrbracket, \llbracket B \rrbracket) = \text{Refresh}(\llbracket A \rrbracket)$  ✓

# Content

## Introduction: SCA

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## The Core Problem: Make & Certify a Device as Secure

Security Certification

Deep Learning Attacks

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## Masking

Security Analysis for a Single Encoding

Computing on Masked Secrets

Security Analysis over Computations

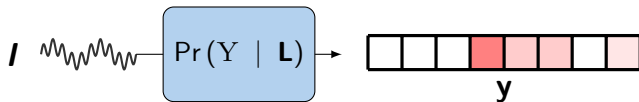
## What about Post-Quantum?

## Perspectives

# Recall on Noisy Leakage Model

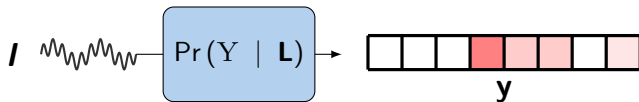
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Yesterday:



# Recall on Noisy Leakage Model

Yesterday:

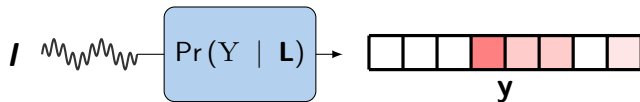


If, the adversary gets:

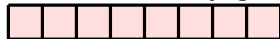


# Recall on Noisy Leakage Model

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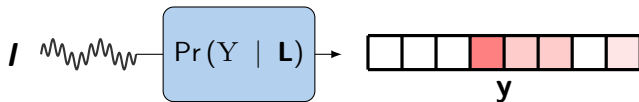


Very noisy leakage

$Y$  indistinguishable from blind guess

# Recall on Noisy Leakage Model

Yesterday:

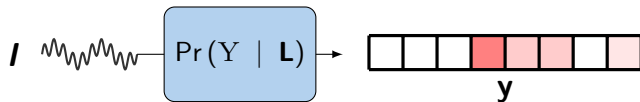


If, the adversary gets:



# Recall on Noisy Leakage Model

Yesterday:



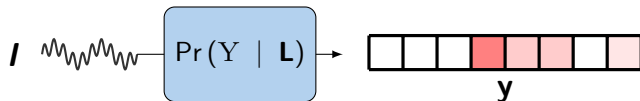
If, the adversary gets:



Low-noise leakage  
Exact prediction for  $Y$

# Recall on Noisy Leakage Model

Yesterday:



## $\delta$ -NOISY ADVERSARY

Any intermediate computation  $Y$  leaks  $L(Y)$  such that:

$$\text{SD}(Y; L) = \mathbb{E}_L \left[ \text{TV} \left( \underbrace{\begin{array}{|c|c|c|c|c|c|c|c|} \hline \text{white} & \text{white} & \text{white} & \text{red} & \text{light red} & \text{light red} & \text{white} & \text{light red} \\ \hline \end{array}}_{\Pr(Y | L)}, \underbrace{\begin{array}{|c|c|c|c|c|c|c|c|} \hline \text{light red} & \text{light red} & \text{light red} & \text{light red} & \text{light red} & \text{light red} & \text{light red} & \text{light red} \\ \hline \end{array}}_{\Pr(Y)} \right) \right] \leq \delta$$



# Security Proof for a Gadget

---

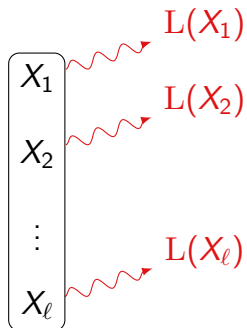
Consider a gadget with  $\ell$  intermediate computations:

$$\begin{pmatrix} X_1 \\ X_2 \\ \vdots \\ X_\ell \end{pmatrix}$$

# Security Proof for a Gadget

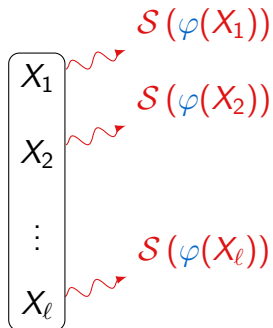
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Consider a gadget with  $\ell$   $\delta$ -noisy intermediate computations:



# Security Proof for a Gadget

Consider a gadget with  $\ell$   $\delta$ -noisy intermediate computations:



**LEMMA (SIMULATABILITY BY RP)**  
*The leakage function  $L$  can be simulated from a **random probing adversary**:  $\varphi(x)$  exactly reveals  $x$  with probability*  

$$\epsilon = 1 - \sum_l \min_x \Pr(L(x) = l) \leq \delta \cdot |\mathbb{F}|.^a$$

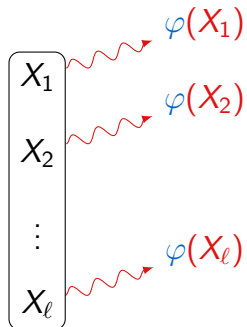
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<sup>a</sup>Duc, Dziembowski, and Faust, “Unifying Leakage Models: From Probing Attacks to Noisy Leakage”.

# Security Proof for a Gadget

---

Consider a gadget with  $\ell$   $\delta$ -noisy intermediate computations:



We may reduce to an adversary observing  $\varphi(X)$  instead of  $\mathcal{S}(\varphi(X))$  (Data Processing Inequality)

# Proof of the Core Lemma (I)

---

Assume there exists such a simulator  $\mathcal{S}$ ,

# Proof of the Core Lemma (I)

---

Assume there exists such a simulator  $\mathcal{S}$ , we need to construct it for all inputs:

$$\begin{aligned}\Pr(\mathcal{S}(x) = l) &= \dots, \text{ for all } x \\ \Pr(\mathcal{S}(\perp) = l) &= \dots\end{aligned}$$

Constraints:

# Proof of the Core Lemma (I)

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→ For all input  $x$ ,  $\Pr(\mathcal{S}(x))$  should be a p.m.f. ( $2 \cdot |\mathbb{F}|$  (in)equations)

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# Proof of the Core Lemma (I)

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- For the input  $\perp$ ,  $\Pr(\mathcal{S}(\perp))$  should be a p.m.f. (2 (in)equations)
- For any  $x, l$ ,  $\Pr(\mathcal{S}(\varphi(x)) = l) = \Pr(L(x) = l)$  ( $|\mathbb{F}| \times |\mathcal{L}|$  equations)

## Proof of the Core Lemma (II)

---

Let us start from the last constraint. For any  $x$  and any  $l$ :

$$\Pr(L(x) = l) = \Pr(\mathcal{S}(\varphi(x)) = l)$$

## Proof of the Core Lemma (II)

---

Let us start from the last constraint. For any  $x$  and any  $l$ :

$$\begin{aligned}\Pr(L(x) = l) &= \Pr(\mathcal{S}(\varphi(x)) = l) \\ &= \Pr(\varphi(x) = x) \cdot \Pr(\mathcal{S}(x) = l) + \Pr(\varphi(x) = \perp) \cdot \Pr(\mathcal{S}(\perp) = l)\end{aligned}$$

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Hence,

$$0 \leq \Pr(\mathcal{S}(\perp) = l) = \frac{\overbrace{\Pr(L(x) = l) - \epsilon \cdot \Pr(\mathcal{S}(x) = l)}^{\text{Should not depend on } x}}{1 - \epsilon}$$

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$$0 \leq \Pr(\mathcal{S}(x) = l) = \frac{\Pr(L(x) = l) - \pi(l)}{\epsilon} \quad (3)$$

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Hence,

$$0 \leq \Pr(\mathcal{S}(\perp) = l) = \frac{\overbrace{\Pr(L(x) = l) - \epsilon \cdot \Pr(\mathcal{S}(x) = l)}^{\text{Should not depend on } x}}{1 - \epsilon} = \frac{\pi(l)}{1 - \epsilon} \quad (2)$$

$$0 \leq \Pr(\mathcal{S}(x) = l) = \frac{\Pr(L(x) = l) - \pi(l)}{\epsilon} \quad (3)$$

Is there any  $\epsilon$  such that  $\geq$  and  $\leq$  are valid?



## Proof of the Core Lemma (III)

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Is there any  $\epsilon$  such that  $\geq$  and  $\leq$  are valid? From (2), and (3), we get

$$0 \leq \pi(l) \leq \Pr(L(x) = l) \text{ for any } x$$

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Furthermore, summing (2) over  $l$ , by definition of probability distributions,

$$\sum_l \pi(l) = \underbrace{\sum_l \Pr(L(x) = l)}_{=1} - \epsilon \cdot \underbrace{\sum_l \Pr(\mathcal{S}(x) = l)}_{=1}$$

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Hence,

$$\epsilon = 1 - \sum_l \pi(l) \geq 1 - \sum_l \min_x \Pr(L(x) = l)$$

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# Security against a Random Probing Adversary

---

To succeed, at least  $d$  out of  $\ell$  wires must be revealed to the adversary:

$$\Pr(\text{Adv. wins}) \leq \Pr(\text{At least } d \text{ wires revealed})$$

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<sup>33</sup>Boucheron, Lugosi, and Massart, *Concentration Inequalities: A Nonasymptotic Theory of Independence*, P.24, and Ex. 2.11.

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*If  $\ell$  wires, each independently revealed with proba.  $\epsilon$ :*

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**Q11:** Prove the inequality from a particular case of Chernoff inequality<sup>33</sup>

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# Putting all Together

---

In our context,  $\ell \leq \mathcal{O}(d^2)$  (for  $\otimes$  gadget), and  $\epsilon \leq \delta \cdot |\mathbb{F}|$ :

## *THEOREM (SECURITY BOUND)*

*For a single gadget with  $\ell \leq \mathcal{O}(d^2)$  intermediate computations:*

$$\text{SD}(k; \mathbf{L}) \leq \mathcal{O}\left((7e \cdot d \cdot \delta \cdot |\mathbb{F}|)^d\right)$$

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*For the whole circuit  $\mathbb{C}$ ,  $d/2$ -region probing<sup>34</sup> security implies*

$$\text{SD}(k; \mathbf{L}) \leq \mathcal{O}\left(|\mathbb{C}| (7e \cdot d \cdot \delta \cdot |\mathbb{F}|)^{d/2}\right)$$

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# Remarks on the Proof

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- “Exponential” security ✓

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<sup>35</sup>Brian, Dziembowski, and Faust, “From Random Probing to Noisy Leakages Without Field-Size Dependence”.

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- “Exponential” security ✓
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  - The  $|\mathbb{F}|$  factor is a proof artifact<sup>35</sup> ✓
  - New constructions with better (even constant) leakage rates<sup>36</sup> ✓

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# Content

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Introduction: SCA

The Core Problem: Make & Certify a Device as Secure

Security Certification

Deep Learning Attacks

Use Case: Polymorphic Implementation

More Evaluation Shortcuts

Masking

Security Analysis for a Single Encoding

Computing on Masked Secrets

Security Analysis over Computations

What about Post-Quantum?

# Masking Post-Quantum Cryptography: Kyber

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- Basic arithmetic over  $\mathbb{Z}_q$ , with  $q$  prime
  - ✓ Friendly with arithmetic masking
- *Fujisaki-Okamoto* transform:
  - ✗ Needs to convert masks A2B: complexity  $\mathcal{O}(d^2 \log(d))$
  - ✗ Needs to convert masks B2A: complexity  $\mathcal{O}(d^2)$
  - ✗ Needs to mask hash functions: very expensive

Slow ops unmasked (NTT) become “fast” with higher-order masking ✓

Fast ops unmasked (rejection in Dilithium) become slow with higher-order masking ✗

Alternative masking-friendly signature schemes proposed (Raccoon) ✓

# Content

Introduction: SCA

---

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Loïc Masure

# Conclusion

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Challenges for masking generally:

- Improving the reduction from noisy leakage to probing security
- Can we prove directly in the noisy model ?
- What about non-independent leakage randomness ?

# Conclusion

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## Challenges for masking generally:

- Improving the reduction from noisy leakage to probing security
- Can we prove directly in the noisy model ?
- What about non-independent leakage randomness ?

## Challenges for masking in PQC:

- Analysis in the RP/noisy model: current implementations deviate from the arithmetic circuit ✓
- Having masking-friendly primitives
- Make masked BIKE affordable
- Masking MQ-like: not thoroughly explored yet ...

# Pointers

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Interested ?

Coron's keynote at CARDIS 23 on masking lattice-based cryptography


Cassiers' keynote at COSADE 23 on masking composability

Nicolas Bordes' thesis with nice examples of probing notions.




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