9th International Symposium on Reconfigurable and Communication-centric Systems-on-Chip











Call For Papers

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Over the past decade ReCoSoC has established itself as a reference international event for research in the areas of reconfigurable and communication-centric systems-on-chip. Its informal and dynamic philosophy encourages technical and scientific interactions of both academic and industrial participants through presentations and special sessions reporting latest advances in the related areas.

All accepted papers will be published in IEEE Xplore. In addition authors of the best papers will be invited to submit an extended versions of their papers for publication in special issue of a journal to be announced at a later point in time.

Topics of interest :

Areas of interest include (but are not limited to) :

- New paradigms for reconfigurable and communication-centric computing
- Reconfigurable and adaptive embedded SoCs
- Communication-centric design techniques at different abstraction levels
- On-chip communication architectures
- Low power design of reconfigurable and multiprocessor SoCs
- Communication-aware multiprocessor embedded systems
 - OS and middleware for reconfigurable and multicore SoCs
- Specification languages and design methodologies
- Verification and evaluation techniques
- Industrial case studies
- (special sessions)

Submission :

Authors are invited to submit contributions as maximum 8 page papers in IEEE format. Contribution(s) have to be submitted electronically through the EasyChair portal of the conference :

https://www.easychair.org/conferences/?conf=recosoc2014

Important Dates (Extended Deadline) :

Submission Deadline (regular papers) : 31st 13th April 2014, 23:59 CEST Notification of Acceptance : 21st 28th April 2014