

Annexe 1 : liste classée des publications

Remarque sur l'ordre des auteurs : pour toutes les publications issues de mon équipe et en l'absence de collaborations extérieures, j'utilise la règle générale suivante : contributeur majeur en 1^{er} (en général le doctorant) suivi des autres contributeurs par ordre alphabétique.

1. Revues scientifiques avec comité de lecture

- [R1] P. Girard et P. Nouet, «Evaluation of currents in the fA range», *Electronics Letters*, Vol. 26(13), pp. 844–845, 1990.
- [R2] P. Girard, P. Nouet et B. Pistoulet, «Single energy e–beam latch activation and deactivation», *Microelectronic Engineering* 12, pp. 129–133, 1990.
- [R3] P. Nouet et P. Girard, «An electron beam controlled latch operating under electron beam testing conditions», *Electronics Letters*, Vol. 28 (1), pp. 39–41, 1992.
- [R4] A. Khalkhal, P. Nouet et P. Girard, «Test cells for evaluation of low currents and capacitances» *Electronics Letters*, Vol. 29 (3), pp. 290–291, 1993.
- [R5] P. Girard, R. Lorival et P. Nouet, «Characterisation of time resolution in electron beam measurements», *Electronics Letters*, Vol. 29 (18), pp. 1641–1643, 1993.
- [R6] C. Landrault et P. Nouet, «Testability improvements using E–Beam controllability: principle and design for Electron-Beam Testability», *Microelectronic Engineering* 31, pp. 47-54, 1996.
- [R7] P. Nouet et A. Toulouse, «Use of test structures for characterization and modelling of inter– and intra–layer capacitances in a CMOS process», *IEEE Trans. on Semiconductor Manufacturing*, pp. 233–241, Vol. 10, No. 2, mai 1997.
- [R8] P. Nouet, L. Latorre et Y. Bertrand, «CMOS monolithic micro–electromechanical sensors», *Preparing for the Future*, vol. 8, No. 1, pp. 16–17, mars 1998.
- [R9] L. Latorre et P. Nouet, «A complete methodology for Electro–Mechanical Characterization of a CMOS Compatible MEMS Technology», *IEICE Transactions on Electronics*, Vol.E82-C, No.4, pp. 582-588, avril 1999.
- [R10] L. Latorre, P. Nouet, Y. Bertrand, P. Hazard¹ et F. Presseccq², «Characterization and modeling of a CMOS compatible MEMS technology», *Sensors and Actuators A*, Vol. 74, pp. 143–147, 1999.
- [R11] L. Latorre, V. Berouille, Y. Bertrand, P. Nouet, “MEMS monolithiques : Application à la mesure du champ magnétique”, *Nano et Micro Technologies : Microcapteurs et Microsystèmes Intégrés*, Hermès, Vol. 1, n° 1, 2000, pp. 9-32.
- [R12] V. Berouille, Y. Bertrand, L. Latorre et P. Nouet, “Test and Testability of a Monolithic MEMS for Magnetic Field Sensing”, *Journal of Electronic Testing: Theory and Applications*, Vol. 17, Issue 5, October 2001, pp. 439-450.
- [R13] L. Latorre, Joonwon Kim³, Junghoon Lee³, P. de Guzman³, J. Le Hyesog³, P. Nouet et Chang-Jin Kim³, “Electrostatic actuation of microscale liquid-metal droplets”, *Journal of Microelectromechanical Systems*, Vol. 11, n° 4, August 2002, pp. 302-308.

1. Schneider Electric, Nanterre

2. CNES, Centre Spatial de Toulouse

3. Mechanical and Aerospace Engineering Department, University of California, Los Angeles (UCLA),USA

- [R14] M. Dardalhon, V. Beroulle, L. Latorre, P. Nouet, G. Perez¹, J. M. Nicot¹ and C. Oudea², “Reliability analysis of CMOS MEMS structures obtained by Front Side Bulk Micromachining”, *Microelectronics Reliability*, Volume 42, Issues 9-11, September-November 2002, Pages 1777-1782.
- [R15] Vincent Beroulle, Yves Bertrand, L. Latorre and P. Nouet, “Monolithic piezoresistive CMOS magnetic field sensors”, *Sensors and Actuators A: Physical*, Volume 103, Issues 1-2, 15 January 2003, Pages 23-32.
- [R16] U. Kac³, F. Novak³, F. Azais, P. Nouet et M. Renovell, “Extending IEEE Std. 1149.4 analog boundary modules to enhance mixed-signal test”, *IEEE Design & Test of Computers*, Vol. 20, n°2, March-April 2003, pp. 32-39.
- [R17] L. Latorre, V. Beroulle et P. Nouet, “Design of CMOS MEMS Based on Mechanical Resonators Using a RF Simulation Approach”, *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, Vol. 23, n°6, pp. 962-967, June 2004.
- [R18] F. Azais et P. Nouet, “Analog and Mixed-Signal Test Bus: IEEE 1149.4 Test Standard”, chapitre dans “Test and Design-for-Testability in Mixed-Signal Integrated Circuits”, Huertas, J.L. (Ed.), Springer (ISBN: 1-4020-7724-6), 2004, 28 pages.
- [R19] F. Azais, B. Caillard, S. Dournelle⁴, P. Salomé⁴ et P. Nouet, “A New Multi-Finger SCR-Based Structure for Efficient On-Chip ESD Protection”, *Microelectronics Reliability*, Elsevier, Vol.45, pp. 233–243, 2005.
- [R20] F. Azais, M. Lubaszewski⁵, P. Nouet, M. Renovell, “A Strategy for Optimal Test Point Insertion in Analog Cascaded Filters”, *Journal of Electronic Testing*, Volume 21, Issue 1, Jan 2005, Page 9-16.
- [R21] V. Puyal, A. Konczykowska⁶, P. Nouet, S. Bernard, S. Blayac⁶, F. Jorge⁶, M. Riet⁶, J. Godin⁶, “DC–100-GHz Frequency Doublers in InP DHBT Technology”, *IEEE Transactions on Microwave Theory and Techniques*, Volume 53, Issue 4, April 2005 Page(s):1338 – 1344.
- [R22] N. Dumas, F. Azais, L. Latorre, P. Nouet, “Electro-thermal Stimuli for MEMS Testing in FSBM Technology”, *Journal of Electronic Testing*, Volume 22, Issue 2, Apr 2006, Pages 189 - 198, ISSN: 0923-8174 (Paper) 1573-0727 (Online).
- [R23] A. Chaehoi, F. Mailly, L. Latorre and P. Nouet, “Experimental and finite-element study of convective accelerometer on CMOS”, *Sensors and Actuators A: Physical*, Volume 132, Issue 1, 8 November 2006, Pages 78-84.
- [R24] N. Dumas, L. Latorre and P. Nouet, “Development of a low-cost piezoresistive compass on CMOS”, *Sensors and Actuators A: Physical*, Volumes 130-131, 14 August 2006, Pages 302-311.
- [R25] N. Dumas, L. Latorre and P. Nouet, “Analysis of offset and noise in CMOS piezoresistive sensors using a magnetometer as a case study”, *Sensors and Actuators A: Physical*, Volume 132, Issue 1, 8 November 2006, Pages 14-20.

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2. EADS L.V., Paris, France

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4. ST Microelectronics, Central R&D, Crolles, France

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6. Alcatel-Thales III-V Lab, Marcoussis, France

- [R26] C. Entringer¹, P. Flatresse¹, P. Galy¹, F. Azais and P. Nouet, “Electro-thermal short pulsed simulation for SOI technology”, *Microelectronics and Reliability*, Volume 46, Issues 9-11, September-November 2006, Pages 1482-1485.
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- [R30] H. Campanella⁵, P. Nouet, A. Uranga⁵, P. de Paco⁵, N. Barniol⁵ and J. Esteve⁵, «Automated on-wafer extraction of equivalent-circuit parameters in thin-film bulk acoustic wave resonators and substrate”, *Microwave and Optical Technology Letters*, Vol. 50, n° 1, Pages : 4-7, 2008, doi:10.1002/mop.22986
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- [R32] H.G. Kerkhoff⁶, X. Zhang⁶, F. Maily, P. Nouet, H. Liu² and A. Richardson², “A Dependable Micro-Electronic Peptide Synthesizer Using Electrode Data”, *VLSI Design*, Hindawi Publishing Corporation, Volume 2008, Article ID 437879, 9 pages, doi:10.1155/2008/437879
- [R33] F. Maily, N. Dumas, N. Pous, L. Latorre, O. Garel⁴, E. Martincic⁴, F. Verjus⁷, C. Pellet⁸, E. Dufour-Gergam⁴, P. Nouet, “Pirani pressure sensor for smart wafer-level packaging”, *Sensors and Actuators A: Physical*, Volume 156, Issue 1, November 2009, Pages 201-207, ISSN 0924-4247, doi: 10.1016/j.sna.2009.02.013
- [R34] H. Campanella⁵, E. Martincic⁴, P. Nouet, A. Uranga⁵, J. Esteve⁵, "Analytical and Finite-Element Modeling of Localized-Mass Sensitivity of Thin-Film Bulk Acoustic-Wave Resonators (FBAR)," *IEEE Sensors Journal*, vol. 9, no. 8, pp. 892-901, Aug. 2009. doi: 10.1109/JSEN.2009.2024858
- [R35] J.R. Manouvrier¹, P. Fonteneau¹, C.A. Legrand¹, P. Nouet, F. Azais, “Characterization of the transient behavior of gated/STI diodes and their associated BJT in the CDM time domain”, *Microelectronics Reliability*, Vol. 49, Iss. 12, Dec. 2009, pp. 1424-1432. doi: 10.1016/j.microrel.2009.06.056
- [R36] Pradarelli, B; Latorre, L; Flottes, M.L.; Bertrand, Y; Nouet, P; “Remote Labs for Industrial IC Testing”, *IEEE Transactions on Learning Technologies*, Volume 2, Issue 4 (October 2009) Pages 304-311, doi: 10.1109/TLT.2009.46

1. ST Microelectronics, Central R&D, Crolles, France

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7. NXP Caen, France

8. IMS, Bordeaux, France

- [R37] B. Pradarelli, L. Latorre, P. Nouet, “Integrated Circuits Testing: Remote Access to Test Equipment for Labs and Engineering”, *International Journal of Online Engineering* 5 (2009), pp. 43-50, *doi:10.3991/ijoe.v5s1.1013*
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- [R39] O. Leman, F. Mailly, L. Latorre and P. Nouet, “Noise analysis of a first-order thermal sigma-delta architecture for convective accelerometers”, *Analog Integrated Circuits and Signal Processing*, 2010, Volume 63, Number 3, Pages 415-423, *doi: 10.1007/s10470-009-9419-2*
- [R40] B. Andò, S. Baglio, C. Trigona, N. Dumas, L. Latorre, and P. Nouet, " Nonlinear mechanism in MEMS devices for energy harvesting applications", *Journal of Micromechanics and Microengineering*, 12 pp., Vol.20, Iss.12, December 2010.
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- [R43] B. Alandry, L. Latorre, F. Mailly and P. Nouet, "A Fully Integrated Inertial Measurement Unit: Application to Attitude and Heading Determination" *IEEE Sensors Journal*, vol.11, no.11, pp.2852-2860, Nov. 2011, *doi: 10.1109/JSEN.2011.2170161*.
- [R44] E.M. Boujamaa, F. Mailly, L. Latorre and P. Nouet, “Improvement of Power Supply Rejection Ratio in Wheatstone-bridge based piezoresistive MEMS”, *Analog Integrated Circuits and Signal Processing*, Vol. 71, Iss. 1, pp. 1-9, 2012, *doi : 10.1007/s10470-010-9504-6*

2. Conférences internationales avec actes et comité de lecture

Critère retenu : on trouvera dans cette rubrique les articles largement diffusés (disponible en ligne pour les plus récents) qui ont été sélectionnés par un comité de lecture et qui ont donné lieu à une présentation orale dans une conférence sélective (rang A).

- [C1] P. Girard, P. Nouet et B. Pistoulet, «Effet de l’irradiation électronique basse énergie sur les composants MOS», *Radiations : effets sur les composants et systèmes (RADECS’89)*, Montpellier, 11-14 sept. 1989, *Annales de Physique*, Vol. 14, 1989, pp. 253–257.
- [C2] P. Girard, P. Nouet et B. Pistoulet, «Low leakage current evaluations for process characterizations», *20th European Solid State Device Research Conference*, Nottingham, W. Eccleston et P.J. Rosser Editeurs, Adam Hilger, 1990, pp. 197–200.
- [C3] P. Nouet, P. Girard et B. Pistoulet, «Towards improvements on VLSI circuit reconfiguration», *IEEE workshop on defect and fault tolerance in VLSI systems*, Grenoble, 5–7 novembre 1990, pp. 175–177.
- [C4] P. Girard, P. Nouet et F.M. Roche, «Simple evaluation of very low currents in process characterization», *IEEE International Conference on Microelectronic Test Structures (ICMTS’91, ISBN : 0-87942-588-1)*, Kyoto, Japan, 18–20 mars 1991, pp. 93–95.

- [C5] P. Charpenel, P. Girard, P. Nouet et H. Martin¹, «Evaluation of zero charging primary energy on insulators in scanning electron microscopy», 2nd European Symposium on Reliability of Electron devices, Failure physics and analysis (ESREF'91), Bordeaux, 7–10 octobre 1991, pp. 693–697.
- [C6] P. Girard, P. Nouet, A. Khalkhal et F.M. Roche, «Evaluations of leakage currents and capacitances on elementary CMOS devices», IEEE ICMTS'93 (ISBN 0–7803–0857–3), Sitges, Barcelone, Espagne, 22–25 mars 1993, pp. 289–292.
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- [C10] A. Khalkhal et P. Nouet, «On–Chip measurement of interconnect capacitances in a CMOS process», IEEE ICMTS'95 (ISBN 0–7803–2065–4), pp. 145–149.
- [C11] P. Nouet et A. Toulouse, «On–Chip Measurements: A Way for Accurate Modeling of Interconnect Capacitances in a CMOS Process», PATMOS'95, Christian Piguet and Wolfgang Nebel Editors, BIS (ISBN 3–8142–0526–X), pp. 290–301, 1995.
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- [C13] P. Nouet et A. Toulouse, «A Test Chip for Interconnect Capacitance Modelling in a CMOS process», IEEE ICMTS'96 (ISBN 0-7803-2783-7), Trento, Italie, 25–28 mars 1996, pp. 61–65.
- [C14] C. Landrault, P. Nouet et A. Toulouse, «Interconnect capacitance modeling based on the on–chip measurement of realistic test patterns», PATMOS'96: 6th International Workshop on Power Timing Modeling Optimization Simulation (ISBN 88–371–0868–0), Bologna, Italy, September 23–25, 1996, pp. 213–220.
- [C15] C. Landrault, P. Nouet et A. Toulouse, «Inter– and intra–layer capacitance modeling in CMOS VLSI designs», Proc. DCIS'96: XI Conf. on Design of Integrated Circuits and Systems (ISBN 84–89.349–83–5), Sitges, Spain, 20–22 novembre 1996, pp. 599–604.
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- [C17] Y. Bertrand, P. Hazard², M. Labrunée³, L. Latorre, P. Nouet et F. Pressecq², «Characterization of a Microsystem Technology for Space Applications: Use and Limitation of Test Vehicles», 2nd Round Table on Micro/Nano Technologies for Space, 15–17 octobre 1997, ESTEC, Noordwijk, Pays–Bas, pp. 225–229 (ESA WPP-132).

1. Hervé Martin, CNET, MEYLAN.

2. Schneider Electric

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- [C21] A. Toulouse, D. Bernard, C. Landrault et P. Nouet, «Efficient 3D modeling for extraction of interconnect capacitances in deep submicron dense layouts», DATE'99, Munich, 9–12 mars 1999, pp. 576–580 (0-7695-0078-1).
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- [C24] S. Baglio¹, L. Latorre et P. Nouet, «Development of Novel Magnetic Field Monolithic Sensors with Standard CMOS Compatible MEMS Technology», Smart Structures and Integrated Systems, Proceedings of SPIE, Newport Beach, CA, USA, March 1-4, 1999, pp. 417-424 (ISBN 0-8194-3142-7).
- [C25] S. Baglio¹, L. Latorre et P. Nouet, «Resonant magnetic field microsensors in standard CMOS technology», IMTC'99: 16th IEEE Instrumentation and Measurement Tech. Conference, (ISBN 0-7803-5276-9), Venice, Italy, May 24-26, 1999, pp. 452-457.
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- [C28] V. Berouille, Y. Bertrand, A. Boyer³, M. Dardalhon, A. Foucaran³, L. Latorre, P. Nouet, “Monolithic integration of a porous silicon humidity sensor”, 15th Design of Circuits and Integrated Systems Conference (DCIS), Montpellier, France, Nov. 21-24, 2000, pp. 862-867.

1. Université de Catania, Italy

2. ATEMI, service commun de l'Université Montpellier II

3. Centre d'Electronique et de Micro-optoélectronique de Montpellier (CEM2)

- [C29] L. Latorre, P. Nouet, J. Kim¹, C.J. Kim⁴, “Electrostatic actuation of microscale liquid metal droplets : Analysis, experiment, and FEM simulation”, MEMS'00 : ASME International Micro-Electro-Mechanical Systems (Vol. 2), Orlando, Florida, USA, November 5-10, 2000, pp. 105-110 (ISBN 0-7918-1900-0).
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- [C37] V. Beroulle, Y. Bertrand, L. Latorre et P. Nouet, “Micromachined CMOS Magnetic Field Sensors With Low-noise Signal Conditioning”, Proc. IEEE International Conference on Micro Electro Mechanical Systems (MEMS'2002, ISBN: 0-7803-7185-2), pp. 256-259, LAS VEGAS, NEVADA, USA, JANUARY 20-24, 2002.
- [C38] V. Beroulle, Y. Bertrand, L. Latorre et P. Nouet, “On the use of an Oscillation-based Test Methodology for CMOS Micro-Electro-Mechanical Systems”, Proc. DATE'2002 (ISBN 0-7695-1471-5), page 1120 (poster), Paris, France, 4-8 Mars 2002.
- [C39] L. Latorre, V. Beroulle, Y. Bertrand, G. Cathebras, M. Spinka et P. Nouet, “On the Use of CMOS On-Chip Mechanical Devices for Spectral Analysis”, EUROSENSORS'02: 16th European Conference of solid-state Transducers, Prague, Czech Republic, September 15-18, 2002, pp. 191-192.

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- [C40] D. Bernard, C. Landrault et P. Nouet, “Interconnect Capacitance Modelling in a VDSM CMOS Technology”, SOC Design Methodologies, Kluwer Academic Publishers (ISBN 1-4020-7148-5), pp. 133-144, 2002.
- [C41] V. Berouille, Y. Bertrand, L. Latorre et P. Nouet, “Noise optimisation of a piezoresistive CMOS MEMS for magnetic field sensing”, SOC Design Methodologies, Kluwer Academic Publishers (ISBN 1-4020-7148-5), pp. 461-472, 2002.
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- [S17] « Challenges in Manufacturing Test of MNT-based Systems », 12th IEEE Int. Mixed Signal Testing Workshop, June, 23rd, 2006 – Edinburgh, UK.
- [S18] « Design for Manufacturing of MNT-based Systems : a test case », Journées Nationales du GDR Micro- et Nano-Systèmes, Lille, 12 octobre 2006.
- [S19] «Conception de Microsystèmes Monolithiques Testables et Robustes aux Variations de Process», Ecole d'hiver Francophone sur les Technologies de Conception des systèmes embarqués Hétérogènes, Grand Hôtel de Paris, Villard-de-Lans, France – 10-12 janvier 2007.

- [S20] «Design for Test of MNT-based Systems: Alternative Test Solutions for reduced Test Costs», MEMUNITY (The MEMS Test Community) workshop, Tuesday, March 6th 2007, CEA-LETI Grenoble.
- [S21] “The future of Embedded Test within the Design for Micro & Nano manufacture NoE” (in collaboration with Prof A. Richardson¹, University of Lancaster), Workshop on Design for Reliability and Manufacturability in MNT, 24 April 2007, Stresa, Italy.
- [S22] “Challenges in MEMS Manufacture Testing and embedded test solutions”, NTC (Northern Test Center) Remote and Embedded Testing Seminar, Pohto, Oulu, Finlande, May 29th, 2007.
- [S23] “Motionless testing of embedded inertial sensors”, Design, Test & manufacturing Technologies for Integrated Micro&Nano systems Summit, Lancaster, October 1st-4th, 2007.
- [S24] “Mécanismes non linéaires dans les dispositifs MEMS pour la récupération d’énergie”, Journées Nationales du GDR Micro- et Nano-Systèmes, Besançon, 19 novembre 2009.
- [S25] “Design and Test of Integrated-MEMS & CMOS-MEMS”, Colloque “Frontier of Engineering France – Japon”, Grenoble, 12 et 13 octobre 2010.

8. Synthèse

	89/07	2008	2009	2010	2011	
Revue Scientifiques avec Comité de Lecture (§ 1)	29	3	5	3	3	43
Conférences Internationales avec Actes et Comité de Lecture (§ 2)	74	11	11	9	9	114
Colloques sans actes ou à audience restreinte (§ 3 et 4)	56	4	2	0	1	63
Divers (Séminaires invités, brevets, rapports de contrat, § 5, 6 et 7)	44	0	2	1	0	47
Total	203	18	20	13	13	267

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